

An Overview of the K Framework

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(joint work with Traian Florin Serbanuta)

Challenges in Programming Language Design / Semantics / Analysis

- Programming languages are continuously born, updated and extended
 - C#, CIL; Java memory model, Scheme R6RS, C1X
 - Concurrency is the norm, not the exception
- Executable specifications could help
 - Design and maintain mathematical definitions
 - Easily test/analyze language updates/extensions
 - Explore/Abstract non-deterministic executions

K Project

- Started in 2003, motivated mainly by teaching programming languages and noticing that the existing semantic frameworks have limitations
- Project thesis:
 - Rewriting gives an appropriate environment to formally define the semantics of real-life programming languages and to test and analyze programs written in those languages.

Overview

- Rewriting logic semantics project
 - How it all started ...
- K framework
 - K definitional style
 - K concurrent rewriting
- Example
 - Challenge language

Rewriting Logic Semantics Project

- Rewriting Logic (RWL)
 - Meseguer 1992
- Rewriting Logic Semantics (RLS) Project
- Advance the use of rewriting logic for defining programming languages, and for executing and analyzing programs written in them
- Participants (probably incomplete list):
 - Wolfgang Ahrendt, Musab Al-Turki, Marcelo d'Amorim, Irina M. Asavoaie, Mihai Asavoaie, Eyvind W. Axelsen, Christiano Braga, Illiano Cervesato, Fabricio Chalub, Feng Chen, Manuel Clavel, Chucky Ellison, Azadeh Farzan, Alejandra Garrido, Mark Hills, Michael Ilseman, Einar Broch Johnsen, Ralph Johnson, Michael Katelman, Laurentiu Leustean, Dorel Lucanu, Narciso Martí-Oliet, Patrick Meredith, Jose Meseguer, Elena Naum, Olaf Owe, Stefan Reich, Grigore Rosu, Andreas Roth, Juan Santa-Cruz, Ralf Sasse, Wolfram Schulte, Koushik Sen, Andrei Stefanescu, Mark-Oliver Stehr, Carolyn Talcott, Prasanna Thati, Traian Serbanuta, Ram Prasad Venkatesan, Alberto Verdejo

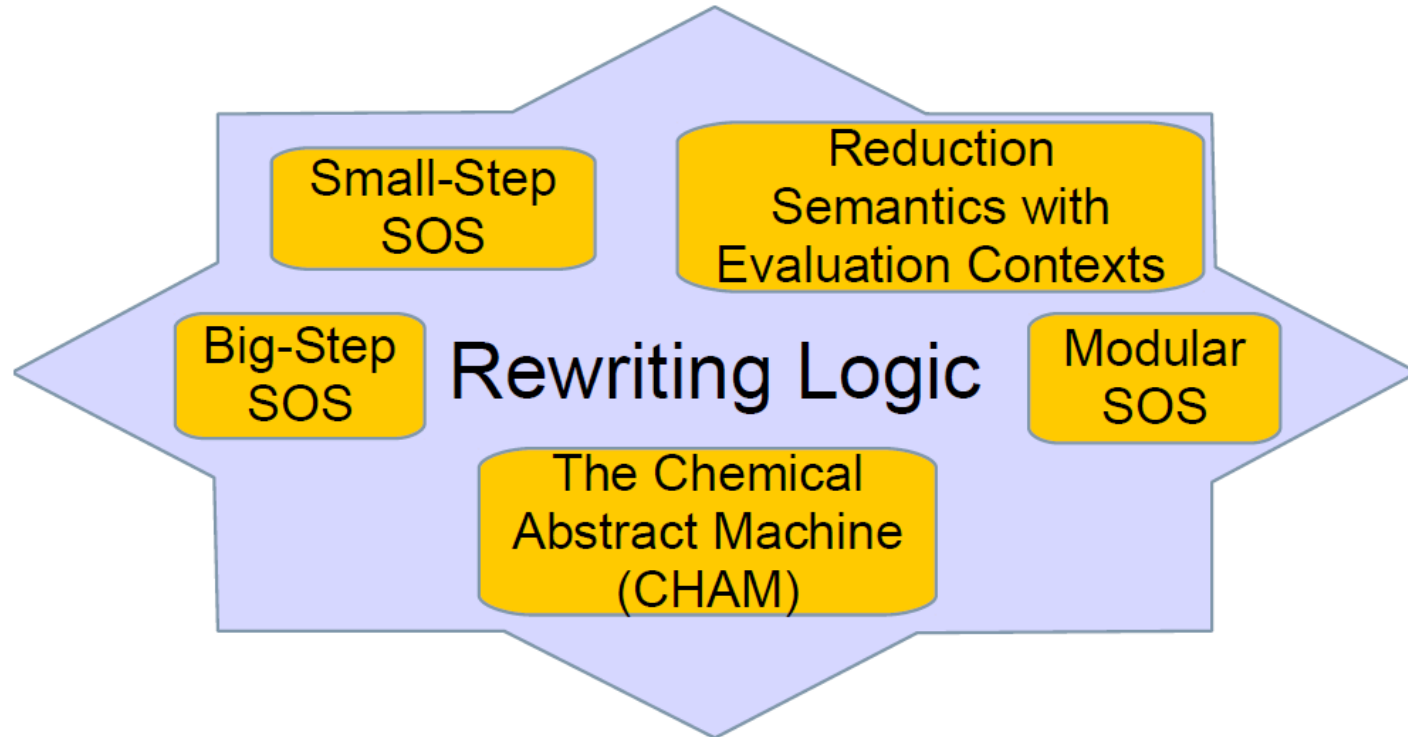
Why is Rewriting Logic Good for Programming Languages ?

- Executability
 - Language definitions turn into interpreters
- Concurrency
 - The norm rather than the exception
- Equational abstraction
 - Collapse state space through equations
- Generic tools (built around Maude)
 - Execution, tracing, debugging, state space search, model checker, inductive theorem prover

Guidelines for Defining Programming Languages in RWL

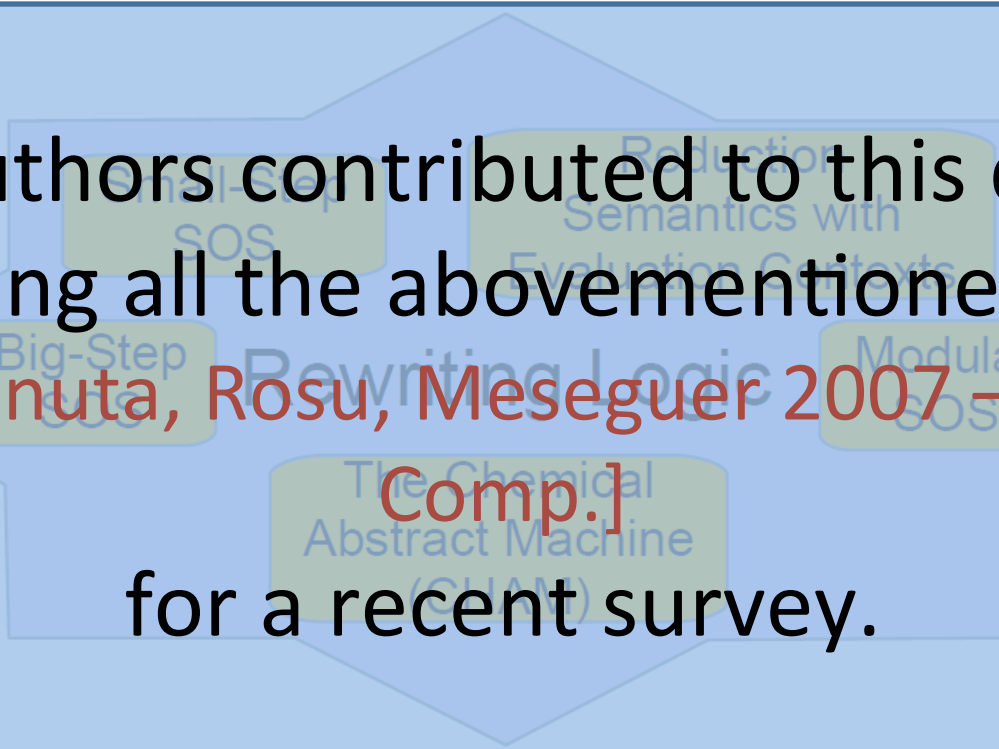
- Represent program state as configuration *term*
- Represent computational steps as *rewrite rules*
- Represent structural changes as *equations*
- These associate to any given configuration a *transition system*
 - In particular, it associates semantics to programs
- Resulting transition systems are amenable to exploration, search and model checking
- Great, but it does not tell us *how* to do it

Conventional semantic frameworks become RWL definitional methodologies



- This allows to define a language using one's favorite semantic style, and then to execute, explore or model check it using RWL

Conventional semantic frameworks become RWL definitional methodologies



Many authors contributed to this diagram,
including all the abovementioned. See
[Serbanuta, Rosu, Mesequer 2007 – Inf. &
Comp.]
for a recent survey.

- This allows to define a language using one's favorite semantic style, and then to execute, explore or model check it using RWL

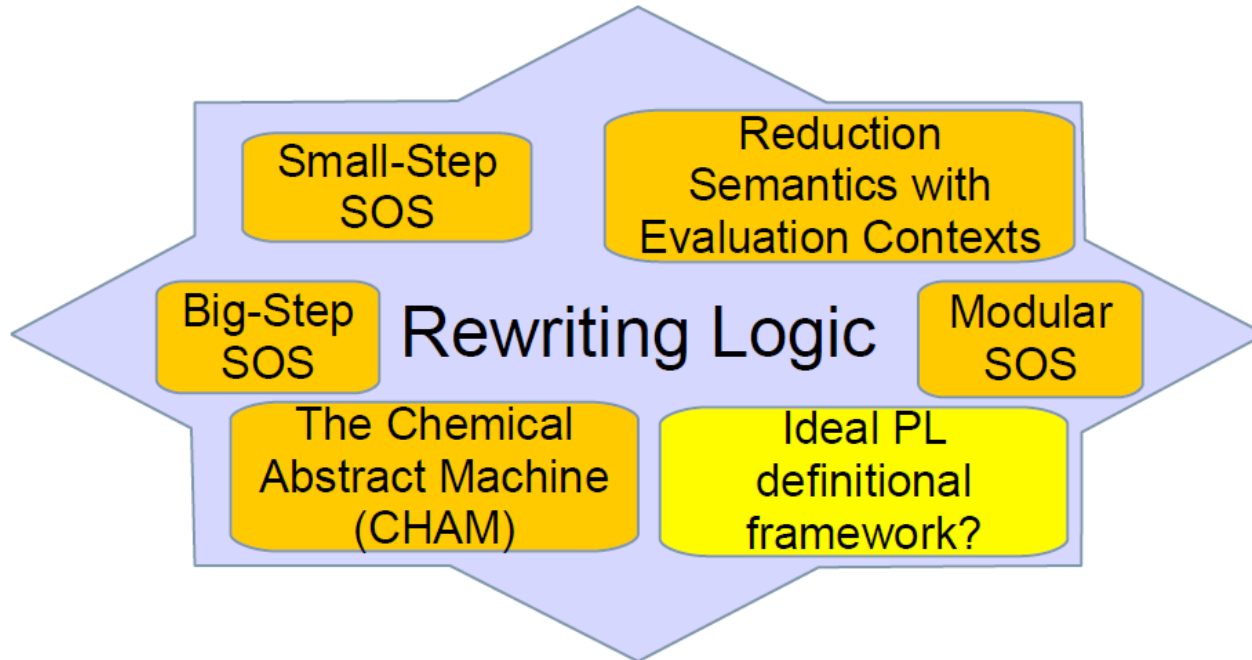
Existing Semantics Frameworks, From a Unified Perspective

- The unified view of semantic frameworks within RWL also allows to better examine them and understand their limitations
- For example, can existing styles define *real* programming languages on a regular basis, as opposed to only toy languages ?
 - No, but their combined strengths might

Shortcomings of Existing Frameworks

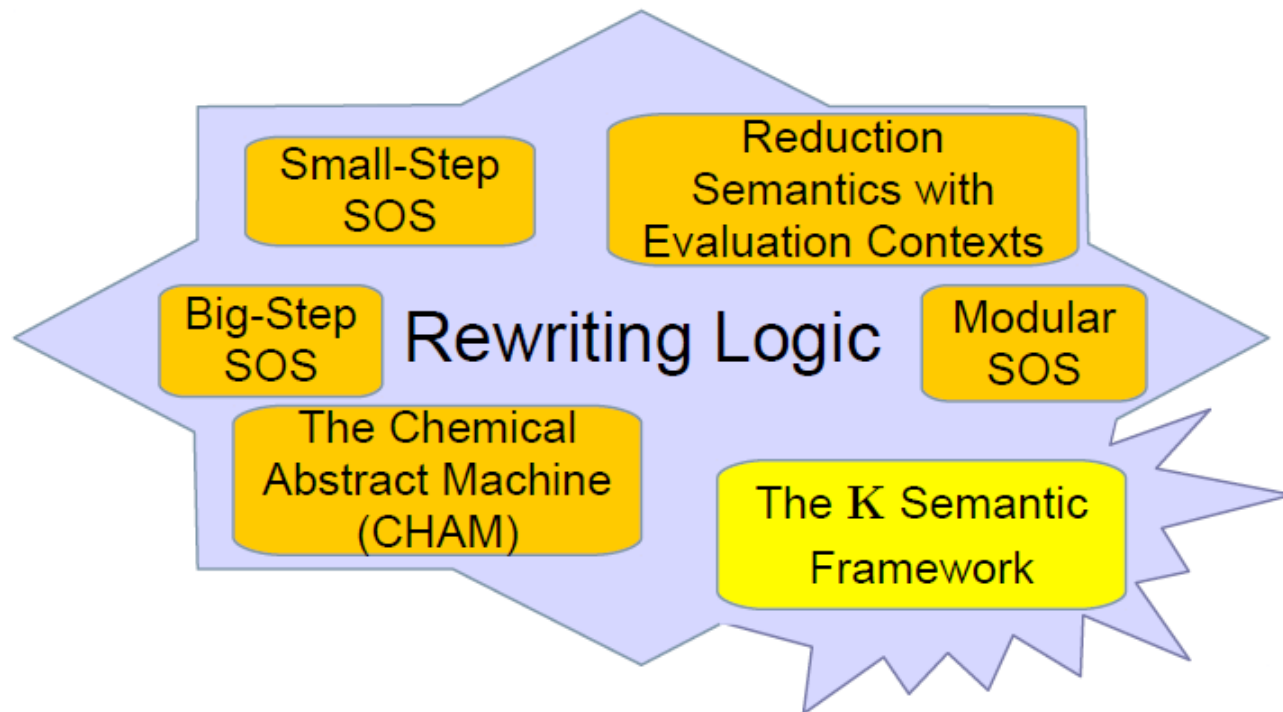
- Hard to deal with control (except evaluation contexts)
 - halt, break/continue, exceptions, callcc
- Non-modular (except Modular SOS)
 - Adding new features require changing unrelated rules
- Lack of semantics for true concurrency (except CHAM)
 - Big-Step captures only all possible results of computation
 - Reduction approaches only give interleaving semantics
- Tedious to find next redex (except evaluation contexts)
 - One has to write the same descent rules for each construct
- Inefficient as interpreters (except for Big-Step SOS)

Towards an Ideal PL Definitional Framework



- Our initial goal was to search for an ideal language definitional framework within RWL
 - At least as expressive as evaluation contexts
 - At least as modular as Modular SOS
 - At least as concurrent as the CHAM

The \mathbb{K} Framework



The \mathbb{K} framework

- \mathbb{K} technique: for expressive, modular, versatile, and clear PL definitions
- \mathbb{K} rewriting: more concurrent than regular rewriting
- Representable in RWL for execution, testing and analysis purposes

\mathbb{K} in a nutshell

Komputations

- Sequences of tasks, including syntax
- Capture the sequential fragment of programming languages
- Syntax annotations specify order of evaluation

Konfigurations

- Multisets (bags) of nested cells
- High potential for concurrency and modularity

\mathbb{K} rules

- Specify only what needed, precisely identify what changes
- More concise, modular, and concurrent than regular rewrite rules

Running example: KERNELC

A subset of the C programming language

- Functions
- Memory allocation
- Pointer arithmetic
- Input/Output

```
void arrCpy(int * a, int * b) {  
    while (* a ++ = * b ++) {}  
}
```

Extended with concurrency features

- Thread creation
- Lock-based synchronization
- Thread join

Running example: KERNELC

MODULE KERNELC-SYNTAX

IMPORTS PL-ID+PL-INT

PointerId ::= Id

Exp ::= Int | PointerId | DeclId

```

  • Exp [ditto]
  • Exp + Exp [strict]
  • Exp - Exp [strict]
  • Exp == Exp [strict]
  • Exp != Exp [strict]
  • Exp <= Exp [strict]
  ! Exp
  Exp && Exp
  Exp || Exp
  Exp ? Exp : Exp
  Exp = Exp [strict(2)]
  printf("Xd;", Exp) [strict]
  scanf("Xd", Exp) [strict]
  & Id
  Id ( Last[Exp] ) [strict(2)]
  Id ()
  Exp ++
  NULL
  free( Exp ) [strict]
  (int*)malloc( Exp * sizeof(int) ) [strict]
  Exp [ Exp ]
  spawn Exp
  acquire( Exp ) [strict]
  release( Exp ) [strict]
  join( Exp ) [strict]

```

SimtLast ::= Simt

Last[Bottom] ::= Bottom

```

  O
  Last[Bottom], Last[Bottom] [id: () strict hybrid assoc]

```

```

  Last[PointerId] ::= PointerId | Last[Bottom]
  Last[PointerId], Last[PointerId] [id: () ditto assoc]

```

```

  Last[DeclId] ::= DeclId | Last[Bottom]
  Last[DeclId], Last[DeclId] [id: () ditto assoc]

```

```

  Last[Exp] ::= Exp | Last[PointerId] | Last[DeclId]
  Last[Exp], Last[Exp] [id: () ditto assoc]

```

DeclId ::= int Exp

```

  void PointerId
  Simt ::= Exp ; [strict]

```

```

  { }
  { SimtLast }
  if( Exp ) Simt
  if( Exp ) Simt else Simt [strict(1)]
  while( Exp ) Simt
  DeclId Last[DeclId] { SimtLast }
  DeclId Last[DeclId] { SimtLast return Exp ; }
  #include< SimtLast >

```

```

  Id ::= main
  Pgm ::= #include<stdio.h>#include<stdlib.h> SimtLast

```

END MODULE

MODULE KERNELC-DESUGARED-SYNTAX

IMPORTS KERNELC-SYNTAX

MACRO: $1 \ E - E \ 7 \ 0 : 1$ MACRO: $E_0 \ \&\& \ E_1 - E_0 \ 7 \ E_2 : 0$ MACRO: $E_0 \ || \ E_1 - E_0 \ 7 \ 1 : E_0$ MACRO: $\text{if}(E) \ St - \text{if}(E) \ St \ \text{else} \ []$ MACRO: $\text{NULL} = 0$ MACRO: $I() = I()()$ MACRO: $DI \ L \{ Sts \} - DI \ L \{ Sts \ \text{return} \ 0 ; \}$ MACRO: $\text{void} \ PI - \text{int} \ PI$ MACRO: $\text{int} \ * \ PI - \text{int} \ PI$ MACRO: $\#include \ Sts > - Sts$ MACRO: $E_0 \ [\ E_1] - * \ E_0 + E_1$ MACRO: $\text{int} \ * \ PI - E - \text{int} \ PI = E$ MACRO: $E ++ - E = E + 1$

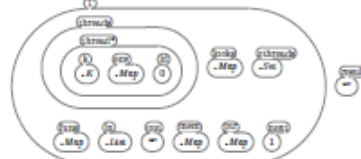
END MODULE

MODULE KERNELC-SEMANTICS

IMPORTS PL-CONVERSION+K+KERNELC-DESUGARED-SYNTAX

 $K \ \text{DeclId} ::= \text{Last} \ \text{Val}$ $K ::= \text{Last} \ \text{Exp} \mid \text{Last} \ \text{PointerId} \mid \text{Last} \ \text{DeclId} \mid \text{SimtLast} \ \text{Pgm} \mid \text{String} \mid \text{resource} \ \text{Map}$ $\text{Exp} ::= \text{Val}$ $\text{Last} \ \text{Exp} ::= \text{Last} \ \text{Val}$ $\text{Val} ::= \text{Int}$ $\text{Int} ::= \text{Int}$ $\text{Last} \ \text{Val} ::= \text{Val}$ $\text{Last} \ \text{Val} ::= \text{Last} \ \text{Val} \mid \text{Id} \ () \ \text{ditto} \ \text{assoc}$ $\text{Last} \ \text{K} ::= \text{Nil} \dots \text{Nil}$

INITIAL CONFIGURATION:



K BASIC:

CONTEXT: $\bullet \square \vdash -$ BASIC: $I_1 \mapsto I_2 \rightarrow \text{GoodObs} \ (I_1 \mapsto_{\text{obs}} I_2)$ BASIC: $I_1 \mapsto I_2 \rightarrow \text{GoodObs} \ (I_1 \mapsto_{\text{obs}} I_2)$ BASIC: $I_1 + I_2 \rightarrow I_1 +_{\text{obs}} I_2$ BASIC: $I_1 - I_2 \rightarrow I_1 -_{\text{obs}} I_2$ BASIC: $I_1 \leftarrow I_2 \rightarrow \text{GoodObs} \ (I_1 \leq_{\text{obs}} I_2)$ BASIC: $I_1 \leftarrow I_2 \rightarrow \text{if}(I_1) \text{obs}$ BASIC: $\text{if}(I) \rightarrow \text{obs} \ St \rightarrow St \quad \text{when } I \mapsto_{\text{obs}} 0$ BASIC: $\text{if}(I) \ St \ \text{else} \rightarrow St \quad \text{when not}_{\text{obs}} \ I \mapsto_{\text{obs}} 0$ BASIC: $V; \rightarrow -$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$ BASIC: $\frac{X \mapsto V}{X \mapsto V}$

```

  (1)  (2)
  X ← V  X ← V

```

```

  (1)  (2)
  int X; X; (No return R;)  X ← int X; X; (No return R;)

```

```

  (1)  (2)
  X (V);  X ← int X; X; (No return R;)

```

CONTEXT: $\text{int} \vdash []$

```

  (1)  (2)
  int X;  X ← 0

```

```

  (1)  (2)
  int X = V;  X ← V

```

```

  (1)  (2)
  V ← resource(St);  St

```

```

  (1)  (2)
  (int*)malloc(N*sizeof(int));  N

```

```

  (1)  (2)
  free(N);  N

```

CONTEXT: $\text{spawn} \vdash []$

```

  (1)  (2)
  spawn X (V);  N

```

```

  (1)  (2)
  X (V);  N

```

```

  (1)  (2)
  join(N);  N

```

```

  (1)  (2)
  acquire(N);  N

```

```

  (1)  (2)
  release(N);  N

```

```

  (1)  (2)
  acquire(N);  N

```

```

  (1)  (2)
  acquire(N);  N

```

```

  (1)  (2)
  N_1 ... N_2 ... int N

```

```

  (1)  (2)
  N_1 ... N_2 ... N_3 ... N

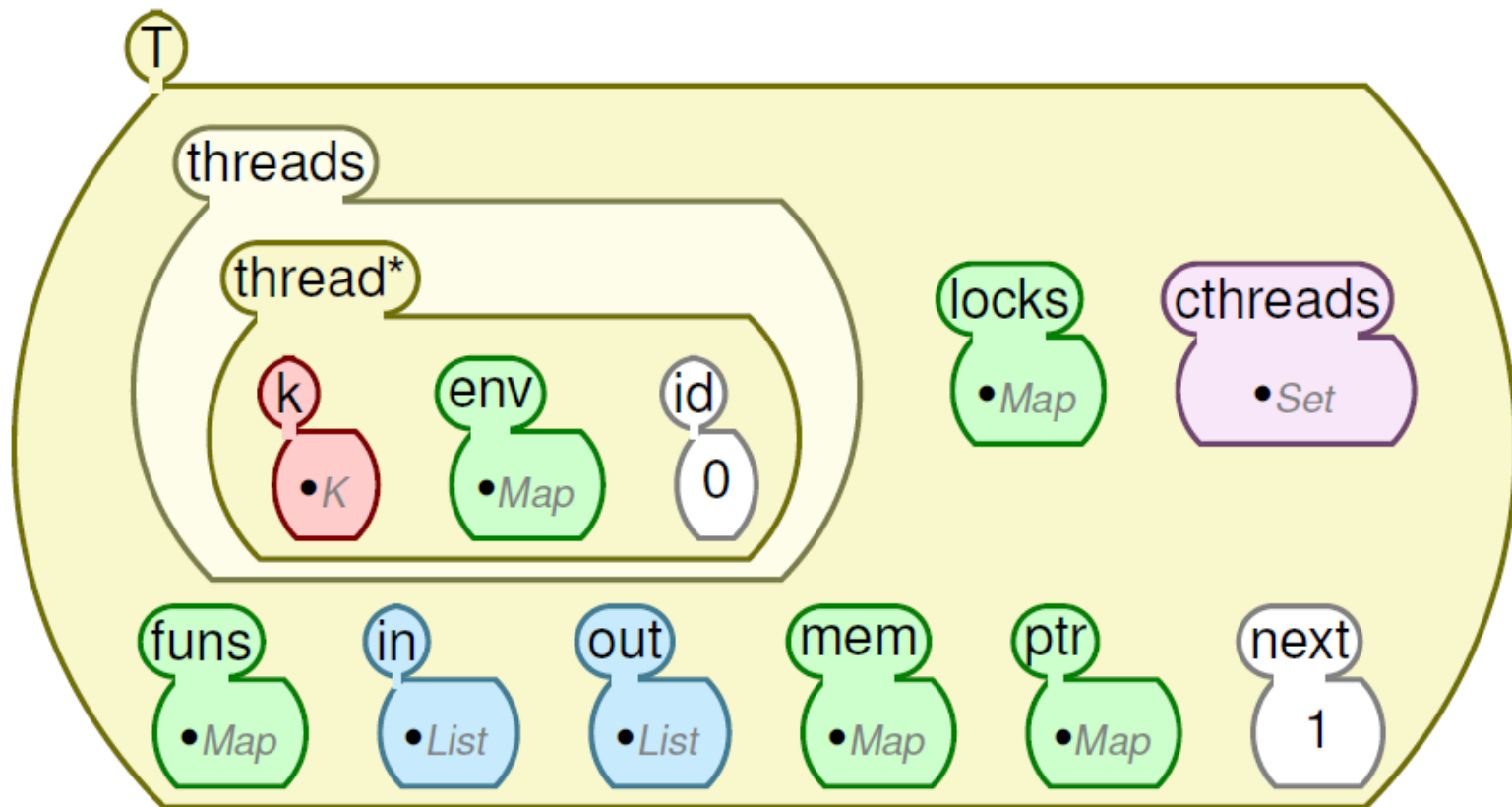
```

END MODULE

Configurations—the running state of a program

- Nested multisets (bags) of labeled cells
 - containing **lists**, **sets**, **bags**, **maps** and **computations**

Initial configuration for **KERNELC**



\mathbb{K} computations and \mathbb{K} syntax

Computations

- Extend PL syntax with a “task sequentialization” operation
 - $t_1 \curvearrowright t_2 \curvearrowright \dots \curvearrowright t_n$, where t_i are computational tasks
- Computational tasks: pieces of syntax (with holes), closures, ...
- Mostly under the hood, via intuitive PL syntax annotations

\mathbb{K} Syntax: BNF syntax annotated with strictness

$Exp ::= Id$

| $* Exp$ [strict]

| $Exp = Exp$ [strict(2)]

$Stmt ::= Exp ;$ [strict]

| $Stmt Stmt$ [seqstrict]

$* ERed \Rightarrow ERed \curvearrowright * \square$

$E = ERed \Rightarrow ERed \curvearrowright E = \square$

$ERed ; \Rightarrow ERed \curvearrowright \square ;$

$SRed S \Rightarrow SRed \curvearrowright \square S$

Heating syntax through strictness rules

Computation

$t = * x ; * x = * y ; * y = t ;$

\mathbb{K} Syntax: BNF syntax annotated with strictness

$Exp ::= Id$

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Heating syntax through strictness rules

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\mathbb{K} Syntax: BNF syntax annotated with strictness

$Exp ::= Id$

$| \ * \ Exp \quad [strict]$

$| \ Exp = Exp \quad [strict(2)]$

$Stmt ::= \textcolor{red}{Exp} ; \quad [strict]$

$| \ Stmt \ Stmt \quad [seqstrict]$

$* \ ERed \Rightarrow ERed \curvearrowright * \square$

$E = ERed \Rightarrow ERed \curvearrowright E = \square$

$ERed ; \Rightarrow ERed \curvearrowright \square ;$

$SRed \ S \Rightarrow SRed \curvearrowright \square \ S$

Heating syntax through strictness rules

Computation

$$t = * x \quad \curvearrowright \quad \square; \quad \curvearrowright \quad \square * x = * y; * y = t;$$

\mathbb{K} Syntax: BNF syntax annotated with strictness

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$$ERed; \Rightarrow ERed \curvearrowright \square;$$

$$SRed S \Rightarrow SRed \curvearrowright \square S$$

Heating syntax through strictness rules

Computation

$$* x \curvearrowright t = \square \quad \curvearrowright \quad \square ; \quad \curvearrowright \quad \square * x = * y ; * y = t ;$$

\mathbb{K} Syntax: BNF syntax annotated with strictness

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$$Stmt ::= Exp ; \quad [strict]$$

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$$* ERed \Rightarrow ERed \curvearrowright * \square$$

$$E = ERed \Rightarrow ERed \curvearrowright E = \square$$

$$ERed ; \Rightarrow ERed \curvearrowright \square ;$$

$$SRed \quad S \Rightarrow SRed \curvearrowright \square \quad S$$

Heating syntax through strictness rules

Computation

$x \curvearrowright * \square \curvearrowright t = \square \curvearrowright \square ; \curvearrowright \square * x = * y ; * y = t ;$

\mathbb{K} Syntax: BNF syntax annotated with strictness

$Exp ::= \textcolor{red}{Id}$

$| * Exp \quad [\text{strict}]$

$| Exp = Exp \quad [\text{strict}(2)]$

$Stmt ::= Exp ; \quad [\text{strict}]$

$| Stmt \quad Stmt \quad [\text{seqstrict}]$

$* ERed \Rightarrow ERed \curvearrowright * \square$

$E = ERed \Rightarrow ERed \curvearrowright E = \square$

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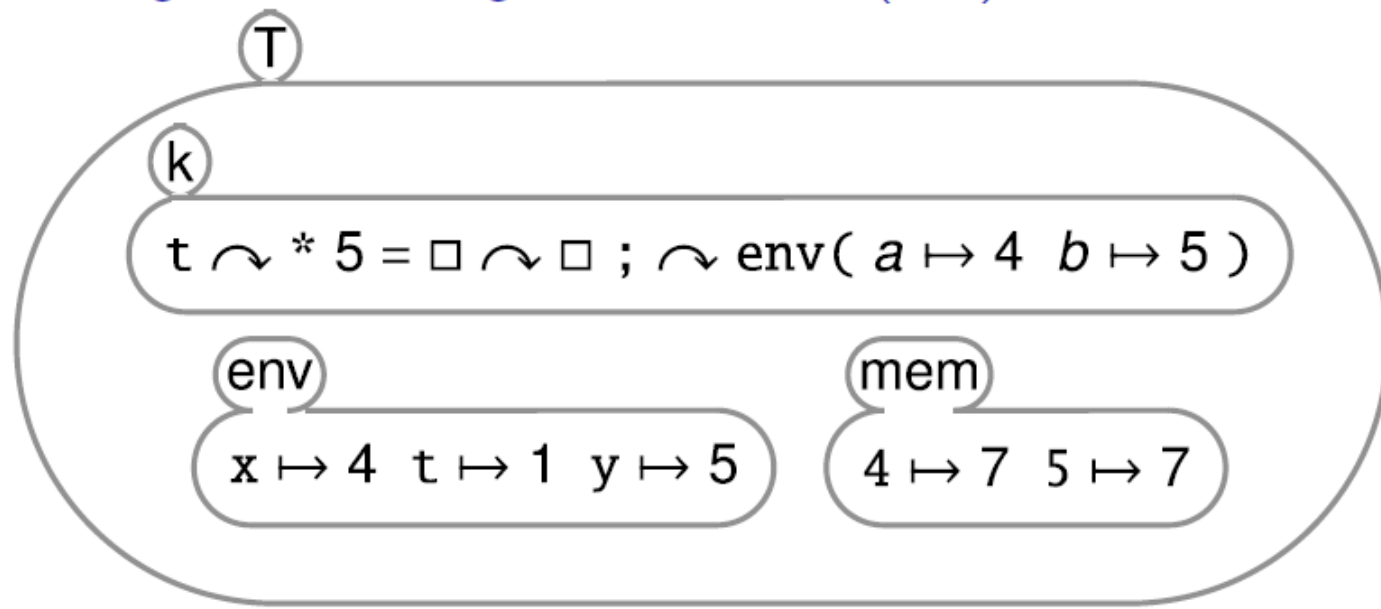
$SRed \quad S \Rightarrow SRed \curvearrowright \square \quad S$

Example: running configuration

Swapping the values at locations 4 and 5 in memory

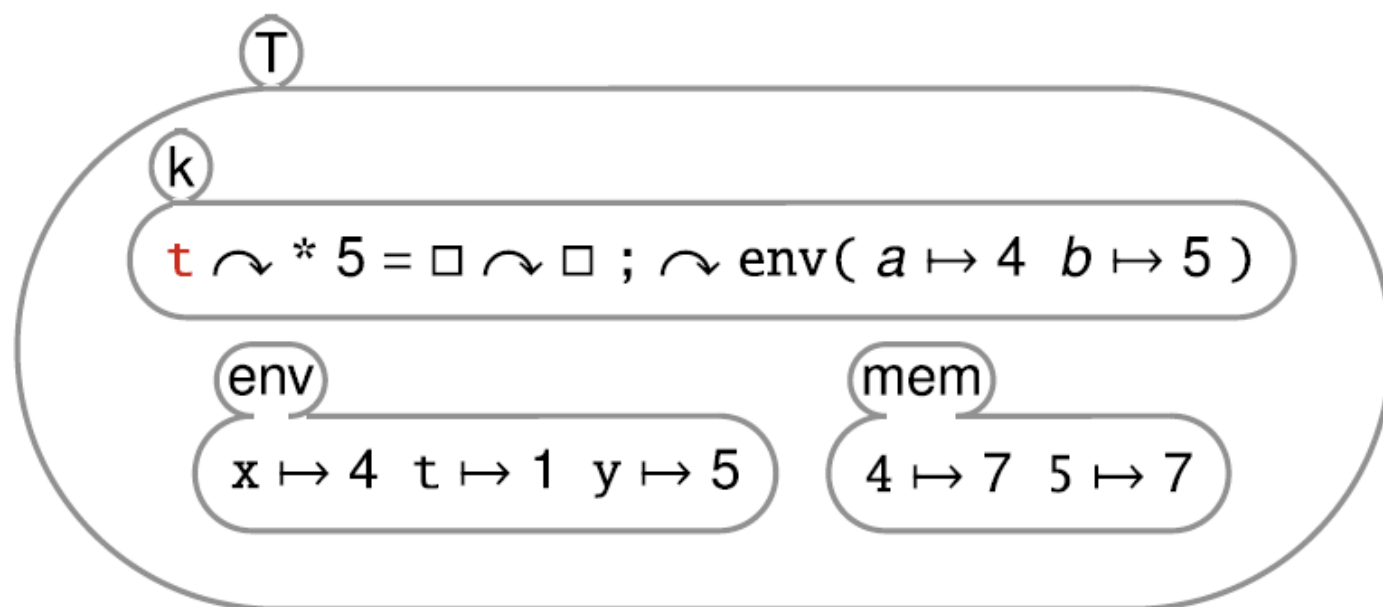
```
void swap(int * x, int * y){
  int t = * x; * x = * y; * y = t;
}
```

Possible Configuration during a call to swap (a, b):



Example: running configuration

- Strictness rules (from annotations) extract the **redex**
- How do we specify next step?

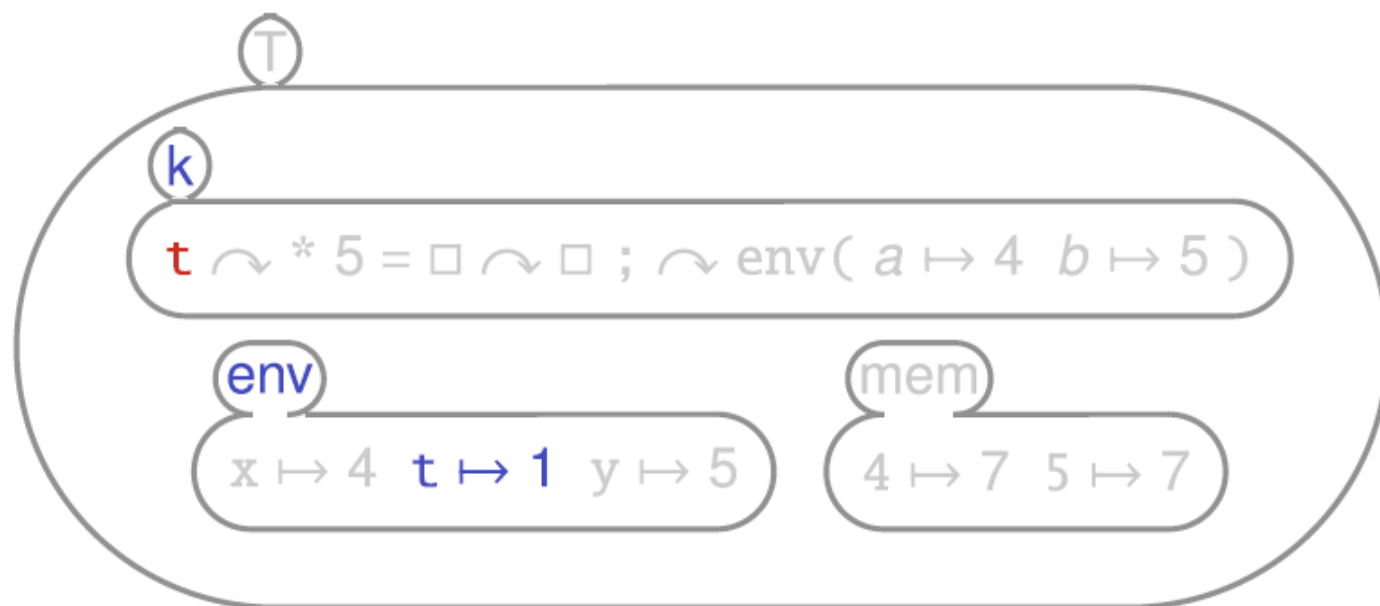


\mathbb{K} rules: expressing natural language into rules

Focusing on the relevant part

Reading from environment

If a local variable X is the next thing to be processed ...
...and if X is mapped to a value V in the environment ...
...then process X , replacing it by V

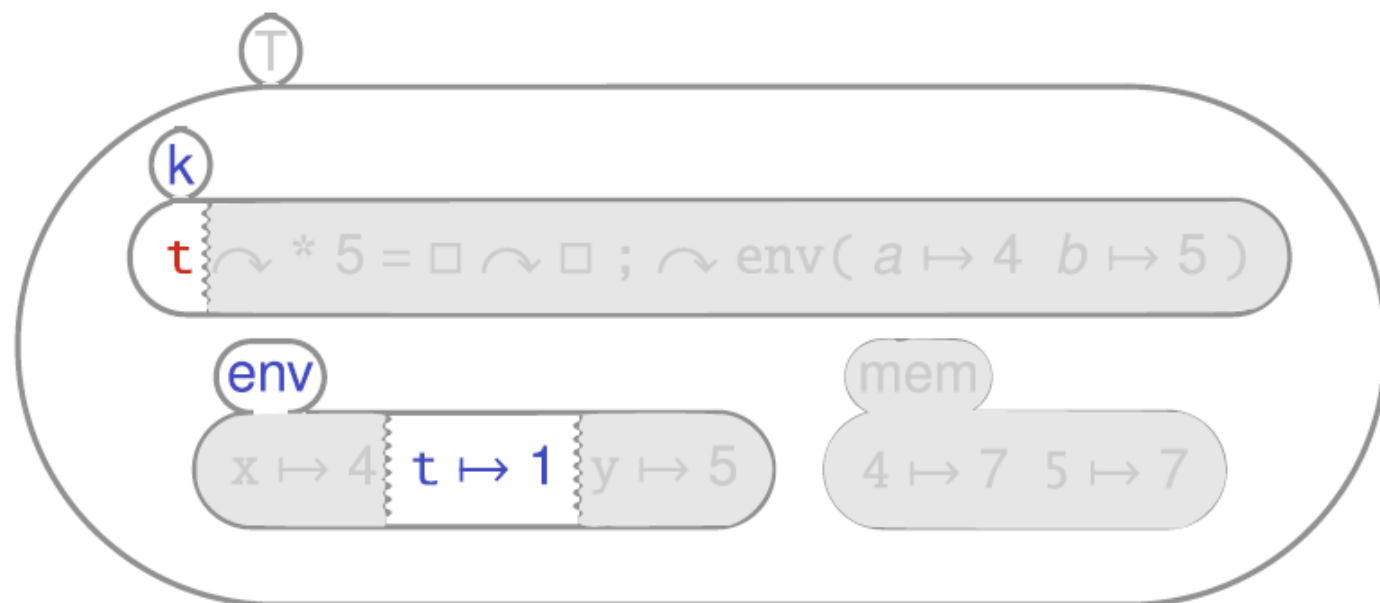


\mathbb{K} rules: expressing natural language into rules

Unnecessary parts of the cells are abstracted away

Reading from environment

If a local variable X is the next thing to be processed ...
 ...and if X is mapped to a value V in the environment ...
 ...then process X , replacing it by V

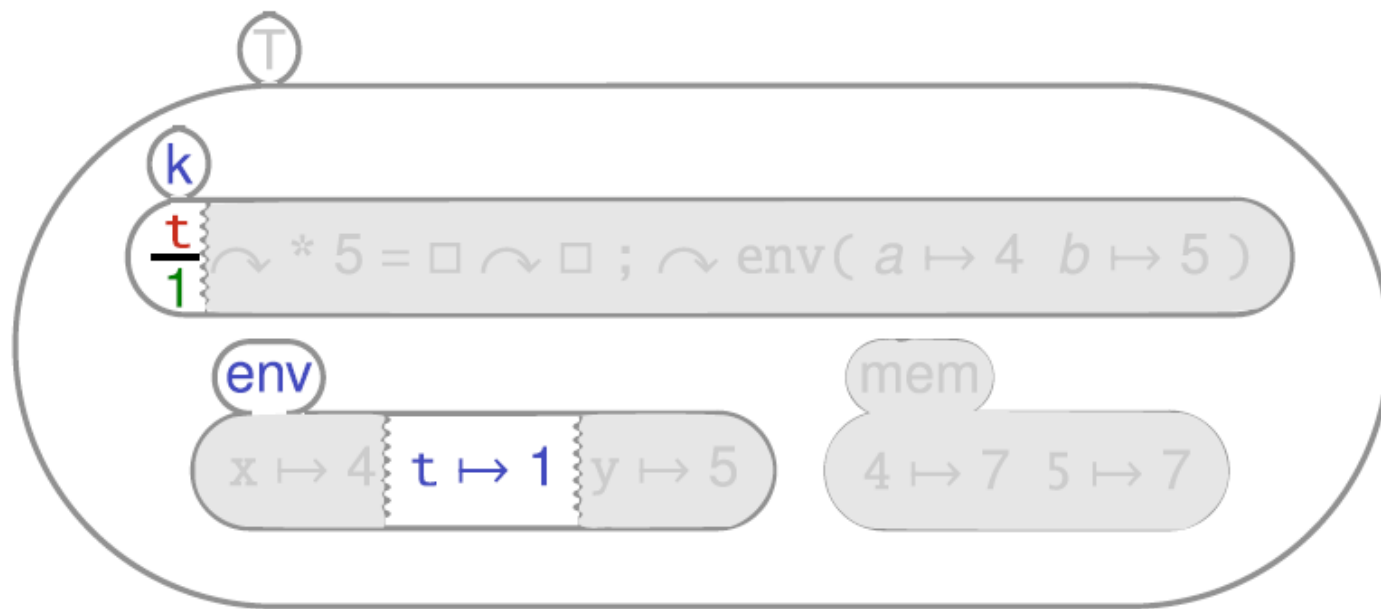


\mathbb{K} rules: expressing natural language into rules

Underlining what to replace, writing the replacement under the line

Reading from environment

If a local variable X is the next thing to be processed ...
 ... and if X is mapped to a value V in the environment ...
 ... then process X , replacing it by V

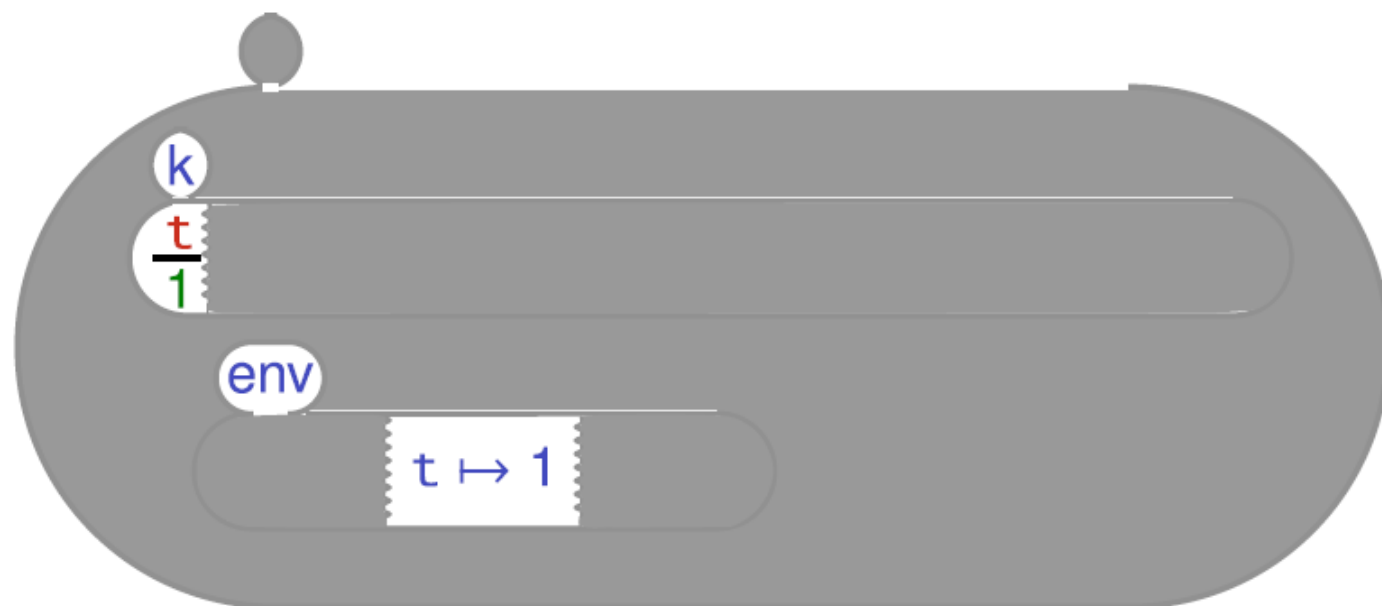


\mathbb{K} rules: expressing natural language into rules

Configuration Abstraction: Keep only the relevant cells

Reading from environment

If a local variable X is the next thing to be processed ...
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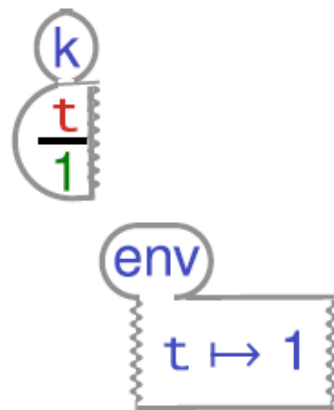


\mathbb{K} rules: expressing natural language into rules

Configuration Abstraction: Keep only the relevant cells

Reading from environment

If a local variable X is the next thing to be processed ...
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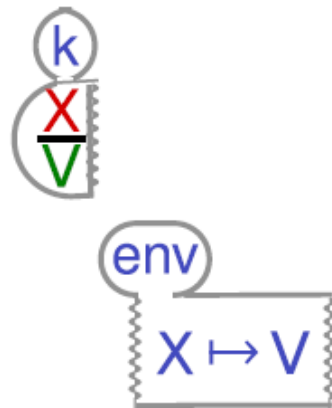


\mathbb{K} rules: expressing natural language into rules

Generalize the concrete instance

Reading from environment

If a local variable X is the next thing to be processed ...
... and if X is mapped to a value V in the environment ...
... then process X , replacing it by V

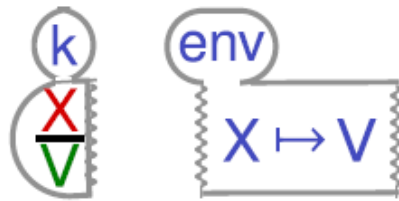


\mathbb{K} rules: expressing natural language into rules

Voilà!

Reading from environment

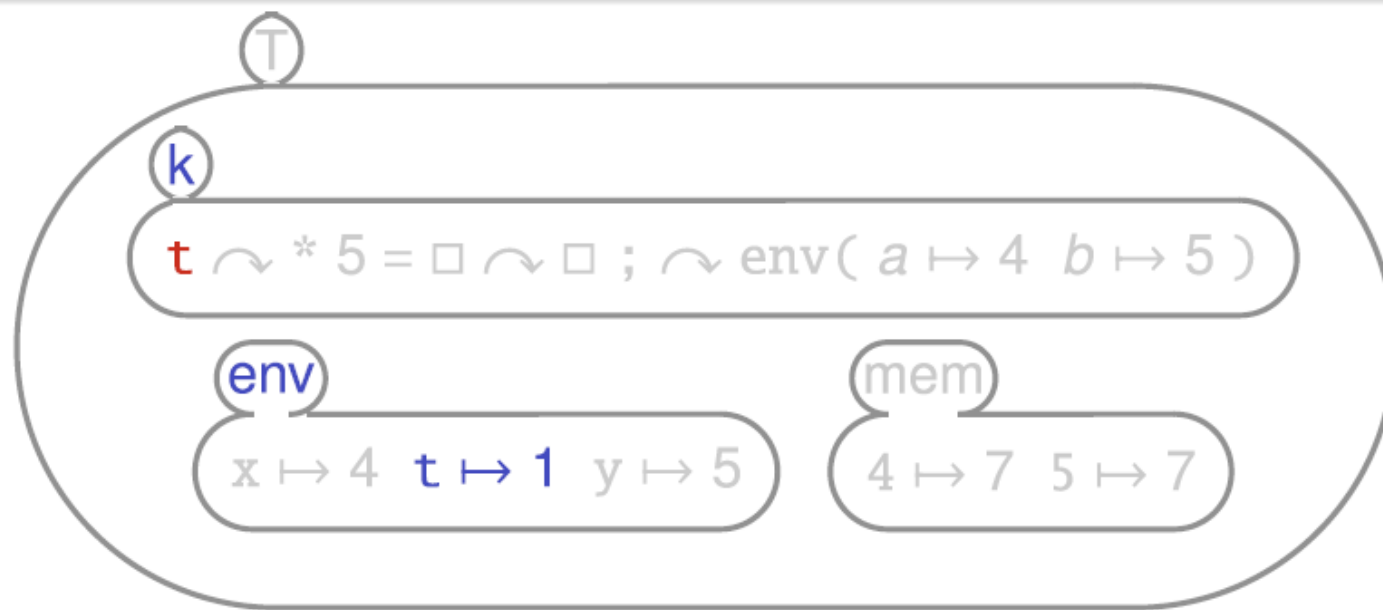
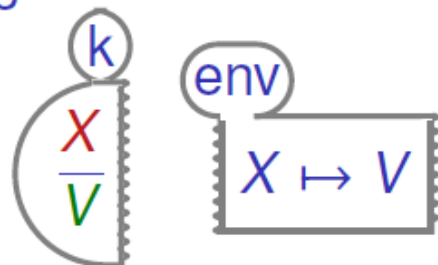
If a local variable X is the next thing to be processed ...
... and if X is mapped to a value V in the environment ...
... then process X , replacing it by V



\mathbb{K} rules: expressing natural language into rules

Summary

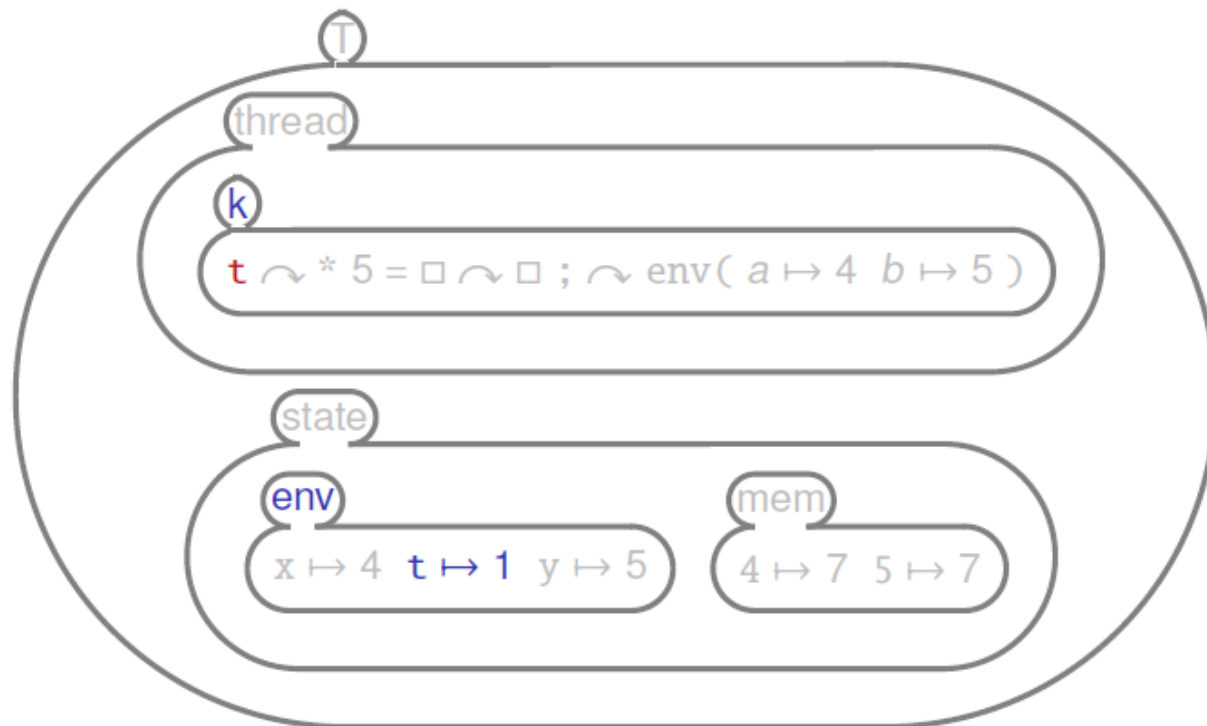
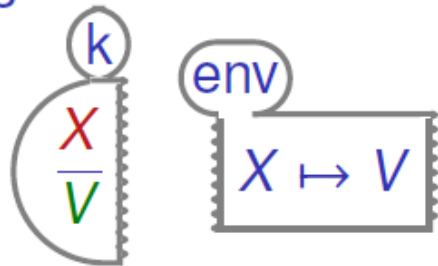
Reading from environment



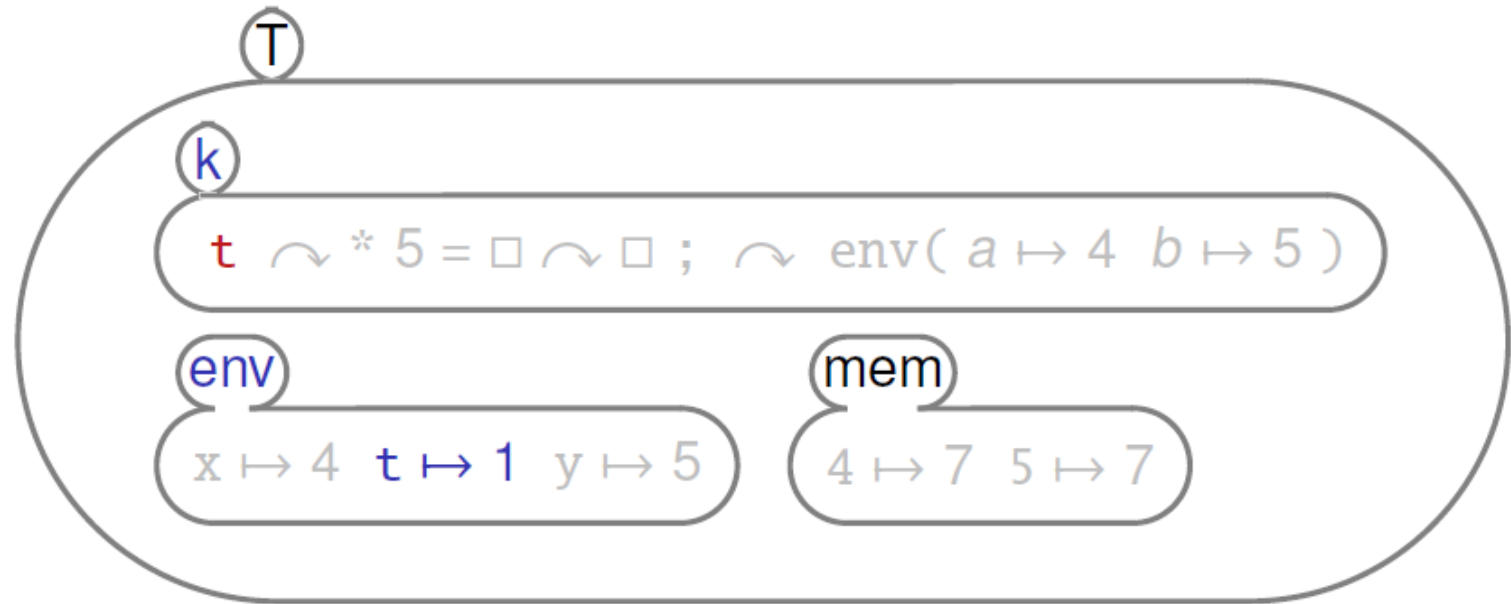
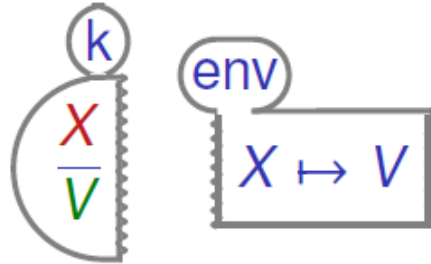
Configuration abstraction enhances modularity

At least as modular as Modular SOS

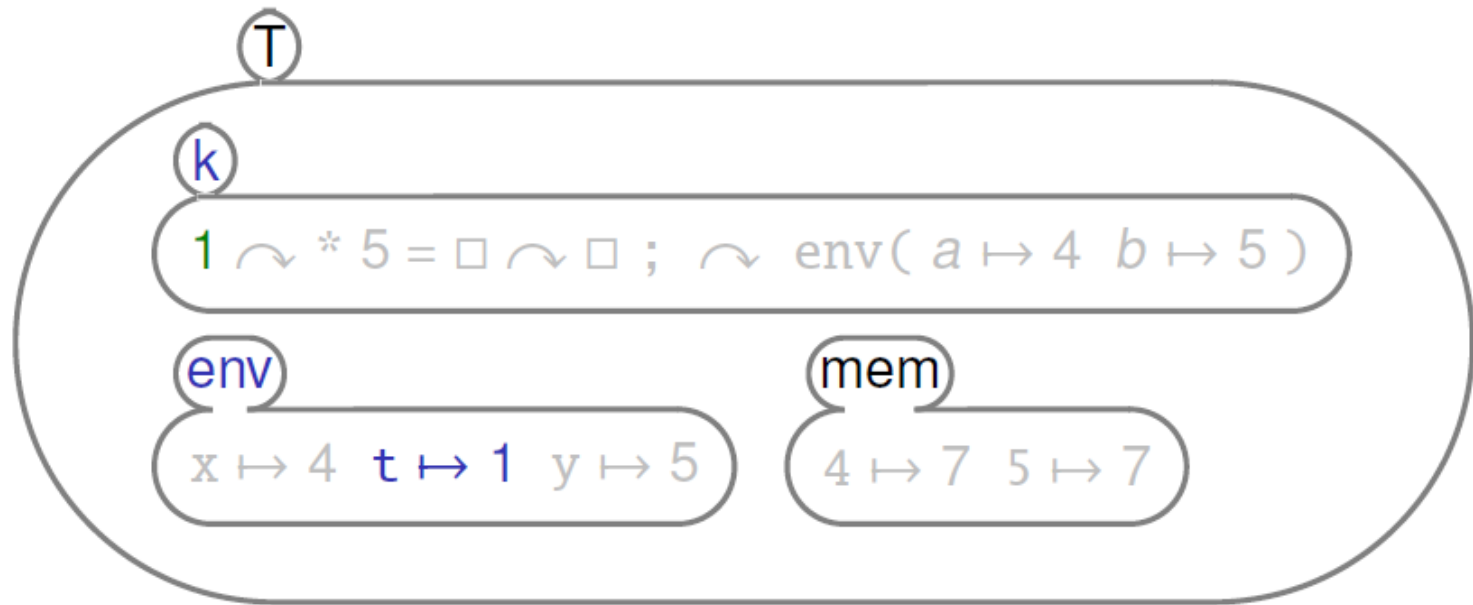
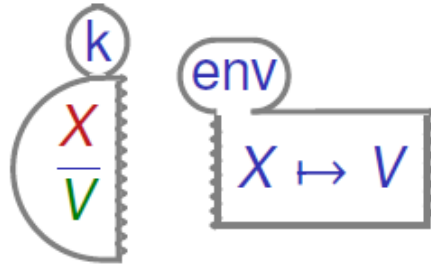
Reading from environment



Applying \mathbb{K} rules: Reading from environment



Applying \mathbb{K} rules: Reading from environment

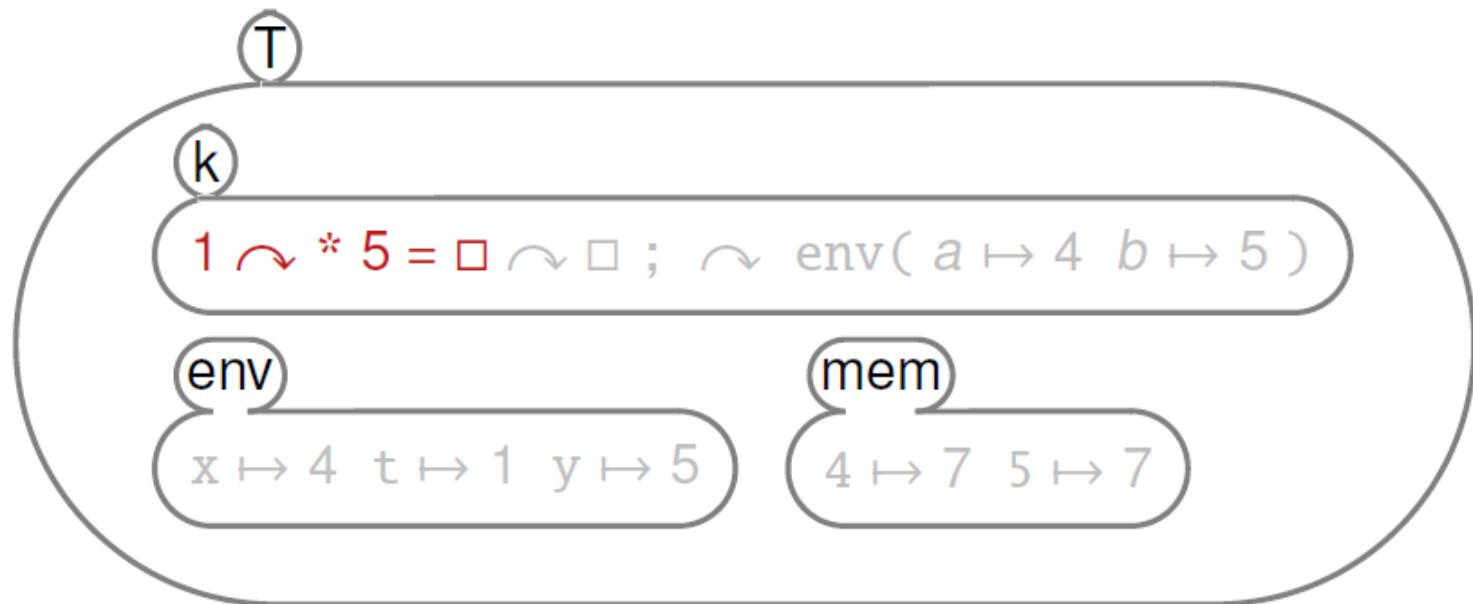


Applying \mathbb{K} rules: Strictness of assignment

$Exp ::= Exp = Exp$ [strict(2)]

which is syntactic sugar for:

$$E = Redex \quad \Rightarrow \quad Redex \curvearrowright E = \square$$

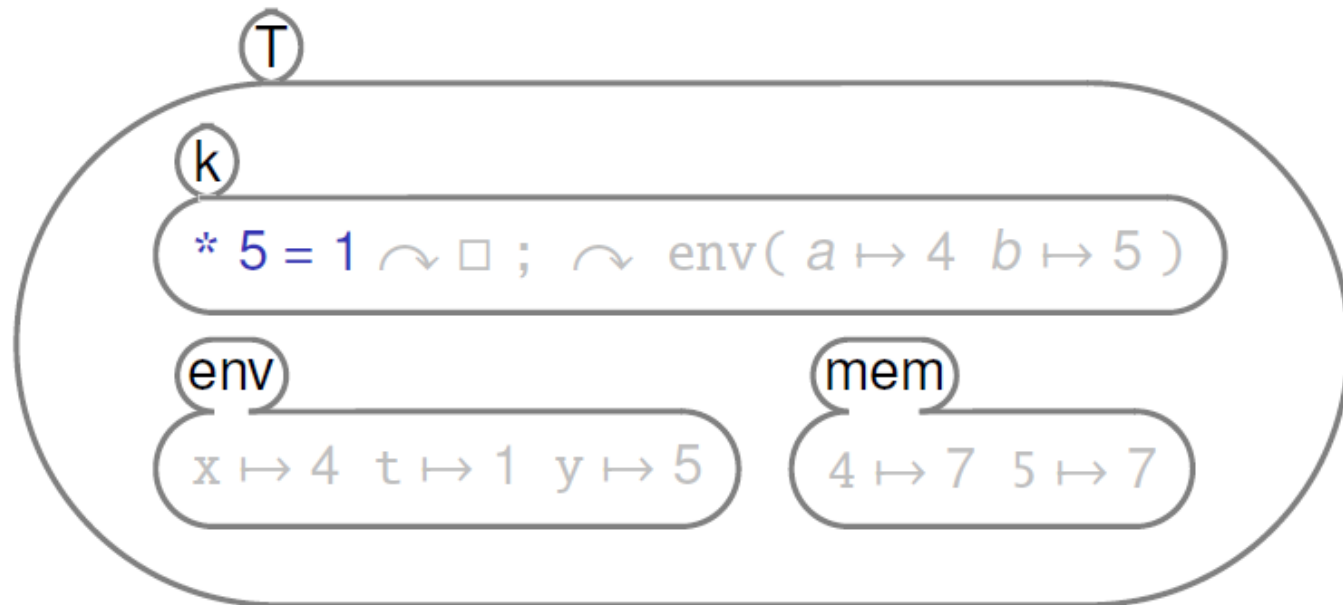


Applying \mathbb{K} rules: Strictness of assignment

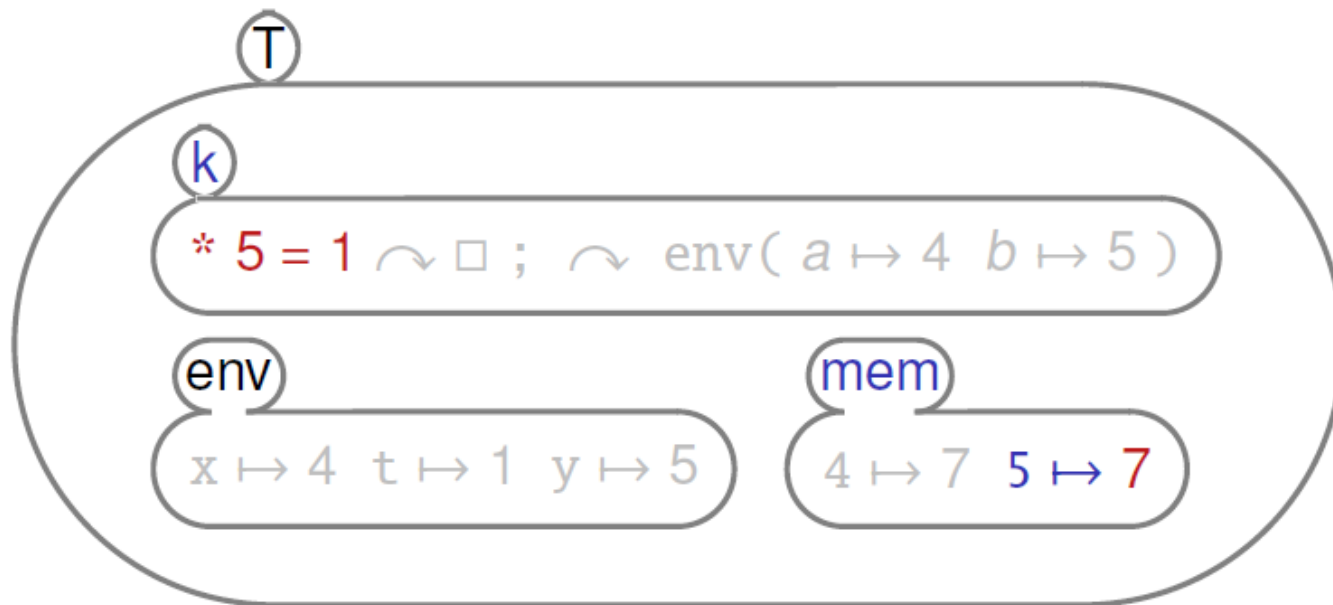
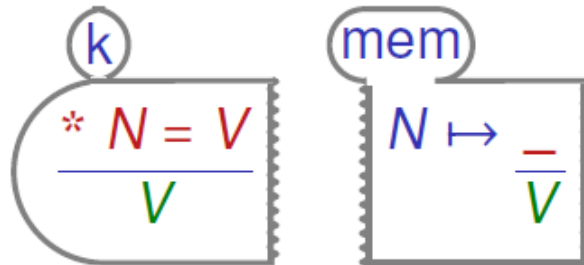
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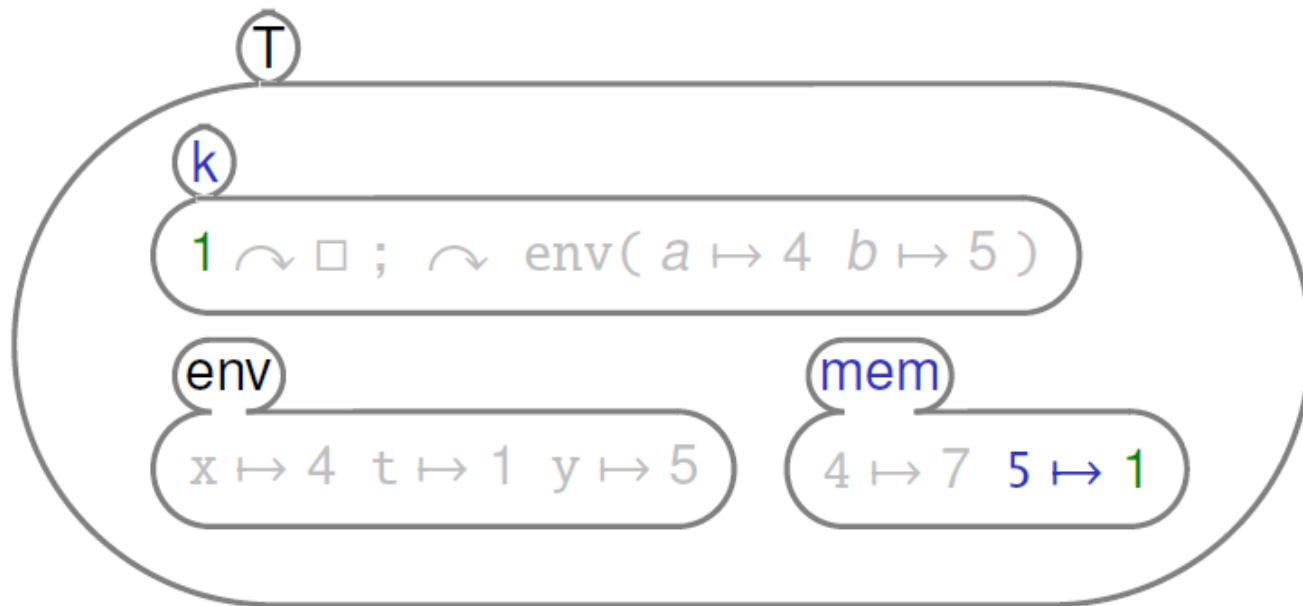
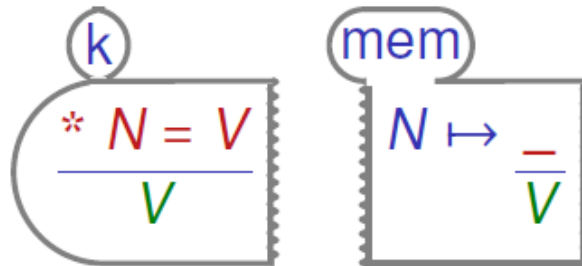
which is syntactic sugar for:

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Applying \mathbb{K} rules: Updating memory

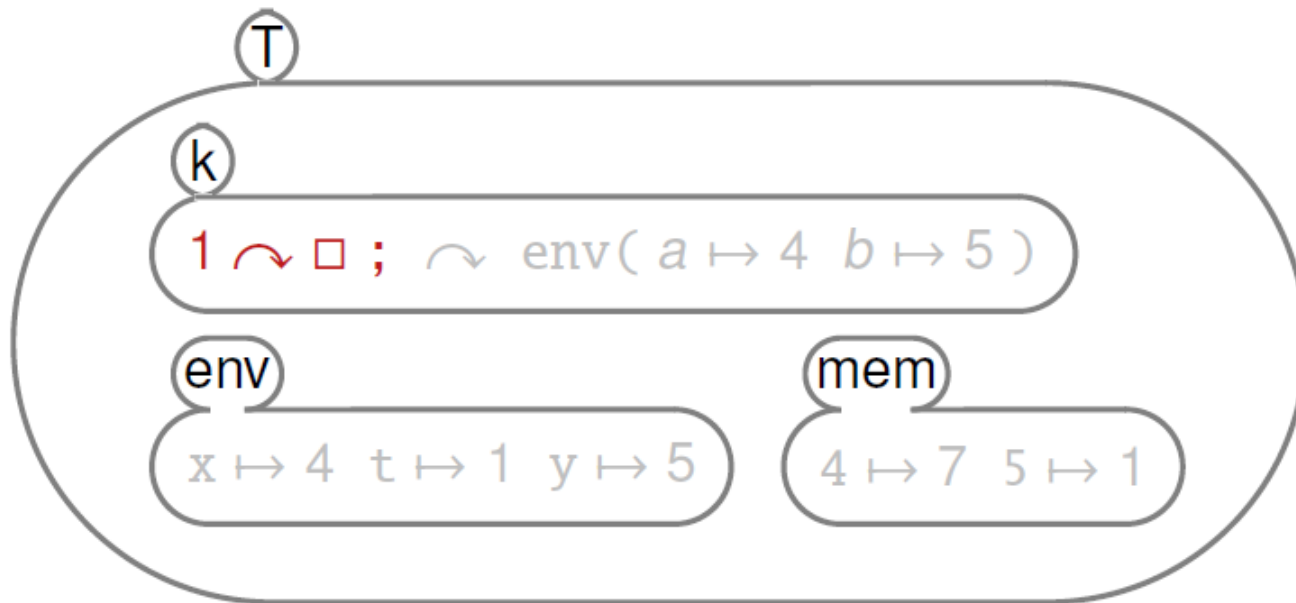


Applying \mathbb{K} rules: Updating memory

Applying \mathbb{K} rules: Semantics for expression statements

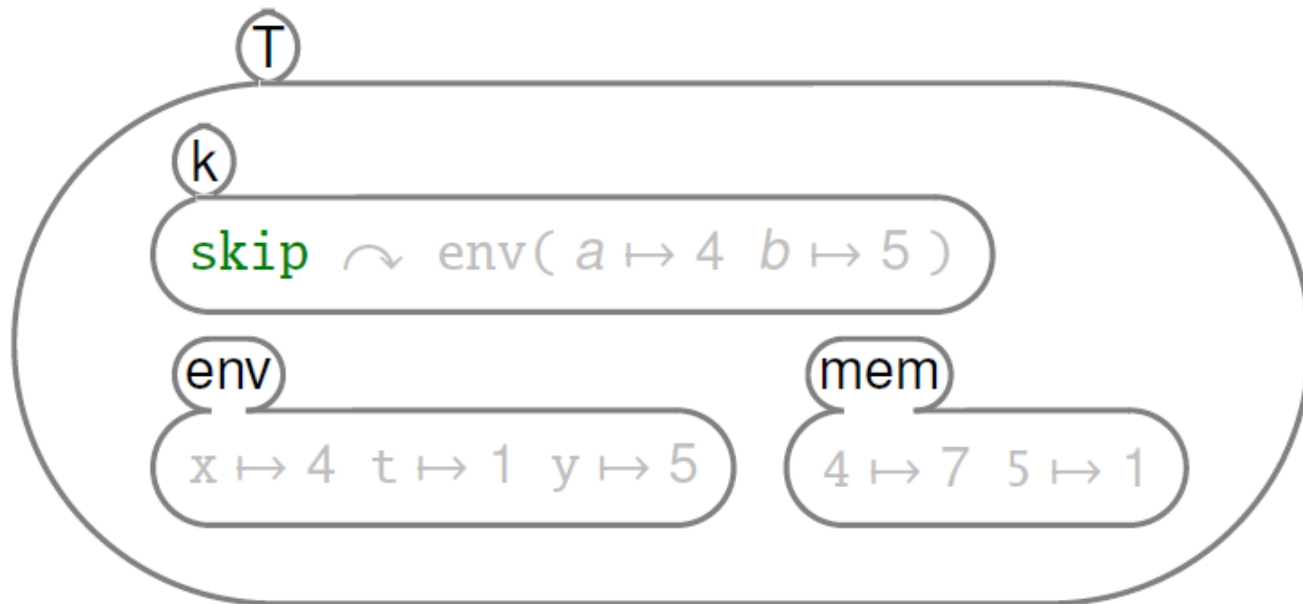
Strictness : $Stmt ::= Exp ;$ [strict]

Semantic rule: $V ; \rightarrow skip$

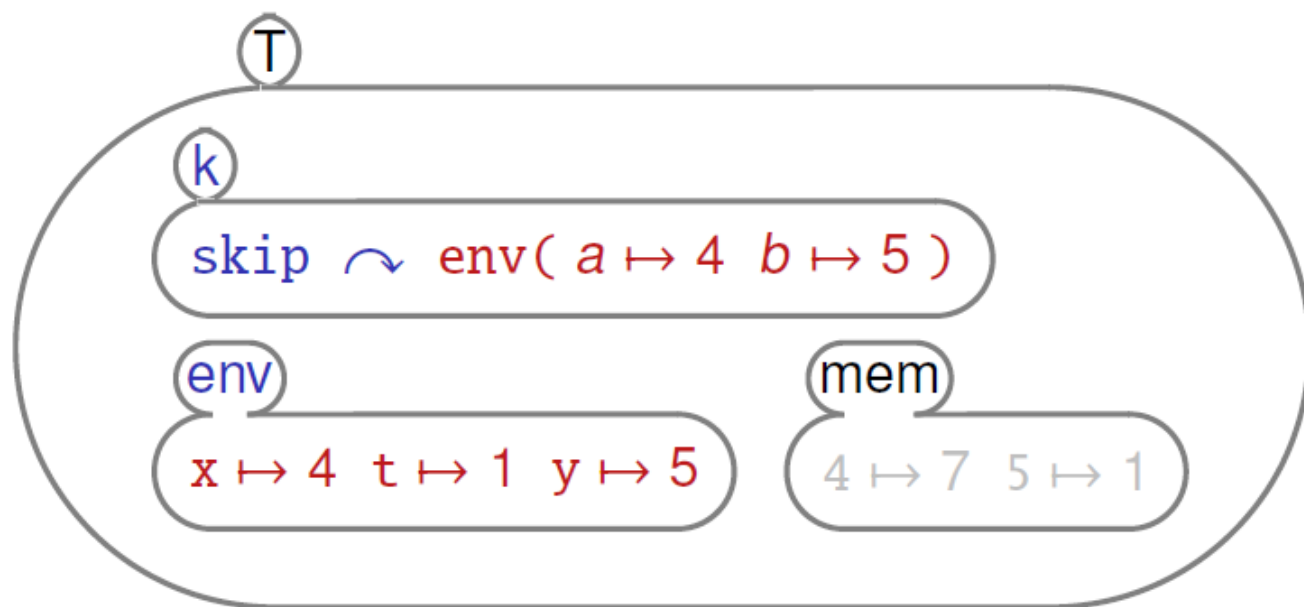
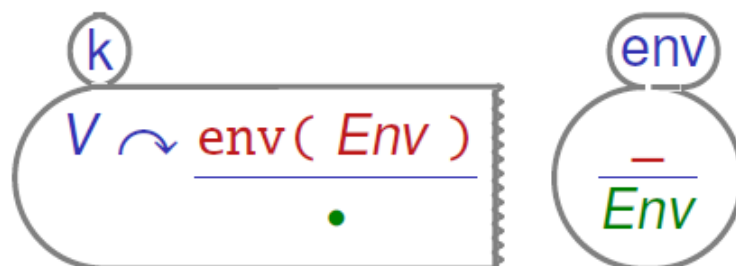


Applying \mathbb{K} rules: Semantics for expression statements

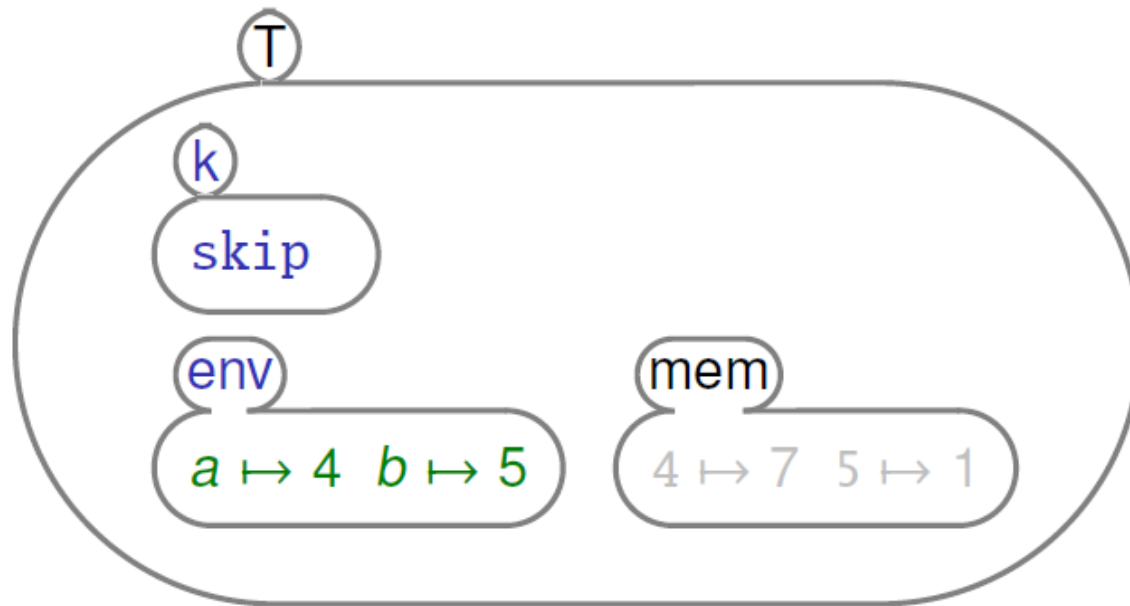
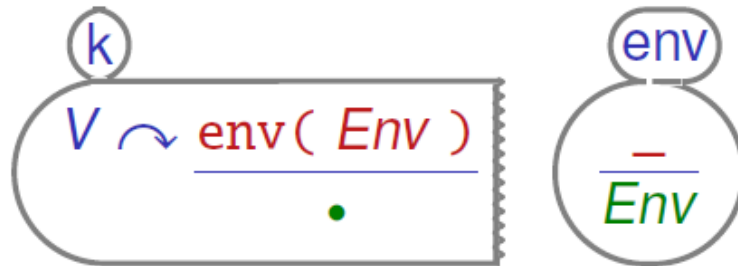
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Applying \mathbb{K} rules: Recovering environment



Applying \mathbb{K} rules: Recovering environment



\mathbb{K} 's definitional power

More expressive than Reduction with Evaluation Contexts

Some of \mathbb{K} 's strengths

- Dealing with control
- Task synchronization
- Defining reflective features

Definitional Power: Control

Call with current continuation

PASSING COMPUTATION AS VALUE:

$$\textcircled{k} \frac{\text{callcc } V \curvearrowright K}{V \text{ cc}(K)}$$

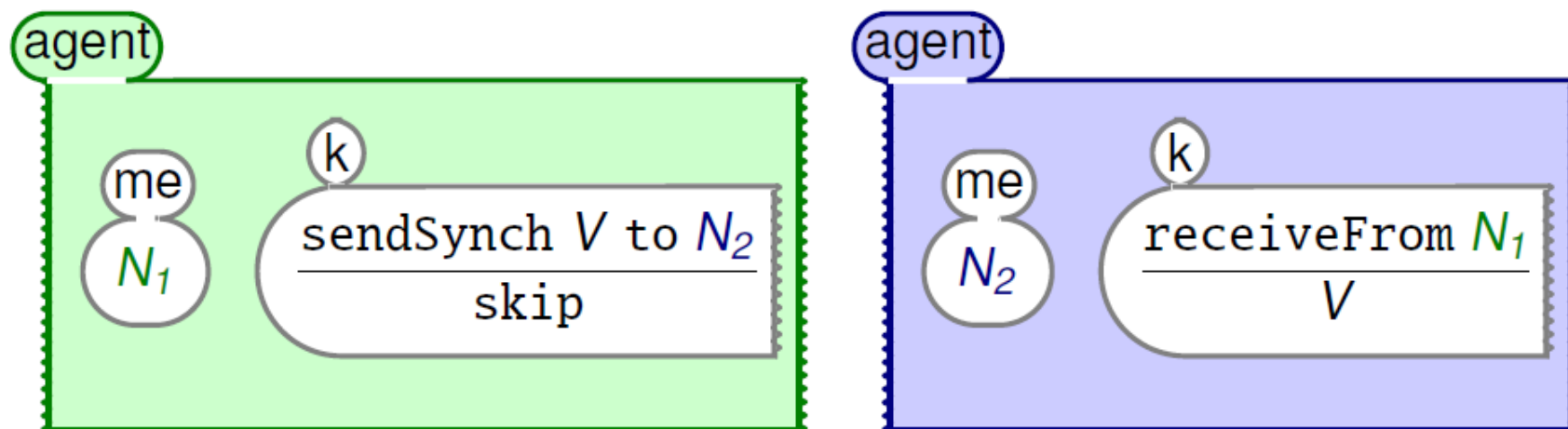
APPLYING COMPUTATION AS FUNCTION:

$$\textcircled{k} \frac{\text{cc}(K) \ V \curvearrowright _}{V \curvearrowright K}$$

- Impossible to capture in frameworks without evaluation contexts
 - Why? Execution context is **logical** context, not observable from within

Definitional Power: Synchronization

Synchronous communication



- Hard, if not impossible to capture in other definitional frameworks
 - (might work for toy languages: CCS, π -calculus)
- Both redexes must be matched simultaneously
 - Easy in \mathbb{K} as redex is always at top of computation

Definitional Power: Syntactic Reflection

\mathbb{K} Syntax = AST

- $K ::= KLabel(List\{K\}) \mid \bullet_K \mid K \curvearrowright K$
 $List\{K\} ::= K \mid \bullet_{List\{K\}} \mid List\{K\}, List\{K\}$
- Everything else (language construct, builtin constant) is a K label

$$a + 3 \equiv _ + _(a(\bullet_{List\{K\}}), 3(\bullet_{List\{K\}}))$$

Reflection through AST manipulation

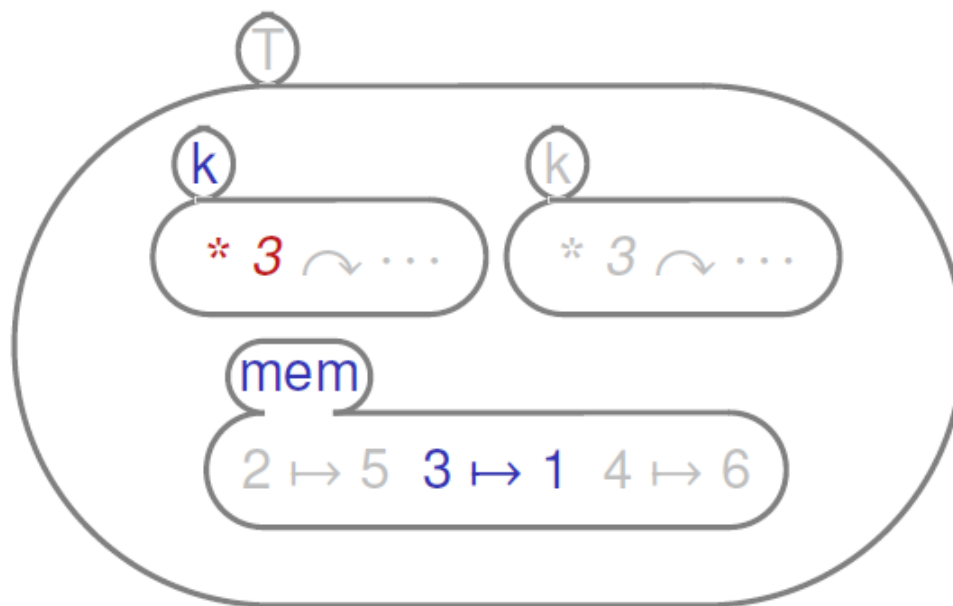
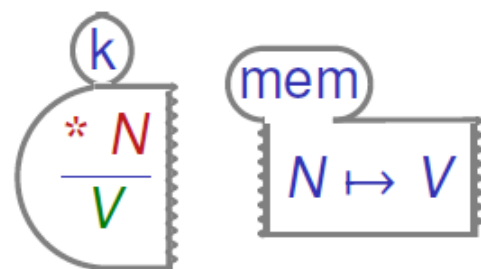
- Generic AST visitor pattern
- Code generation: quote/unquote
- Binder independent substitution

The concurrency of the \mathbb{K} framework

- Truly concurrent (even more concurrent than the CHAM framework)
- Captures concurrency with sharing of resources.

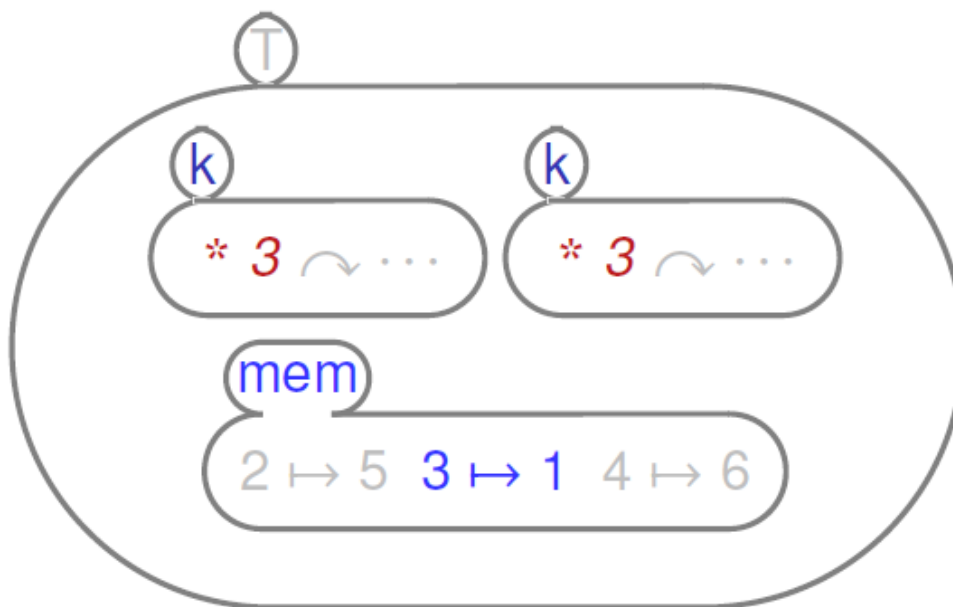
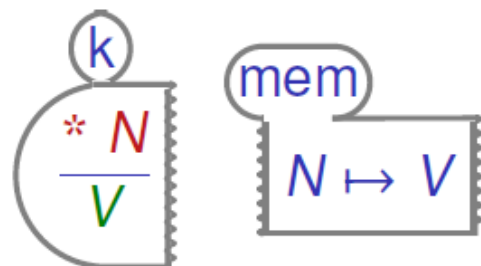
\mathbb{K} rules enhance concurrency

Concurrent reads



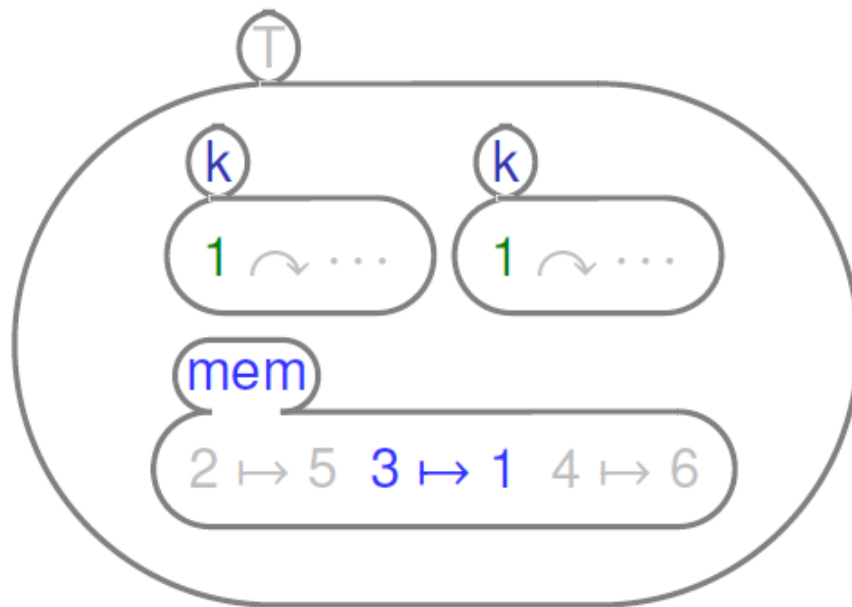
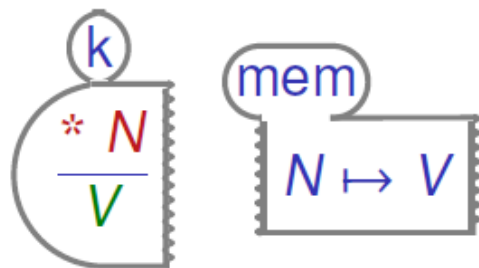
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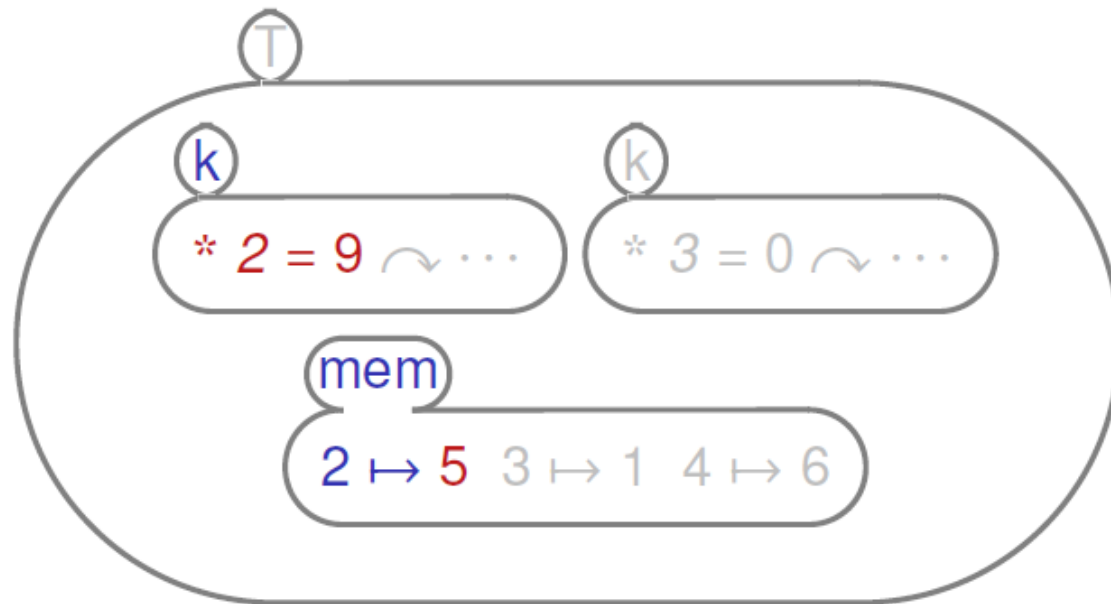
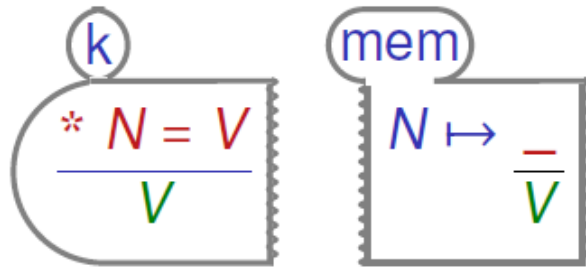
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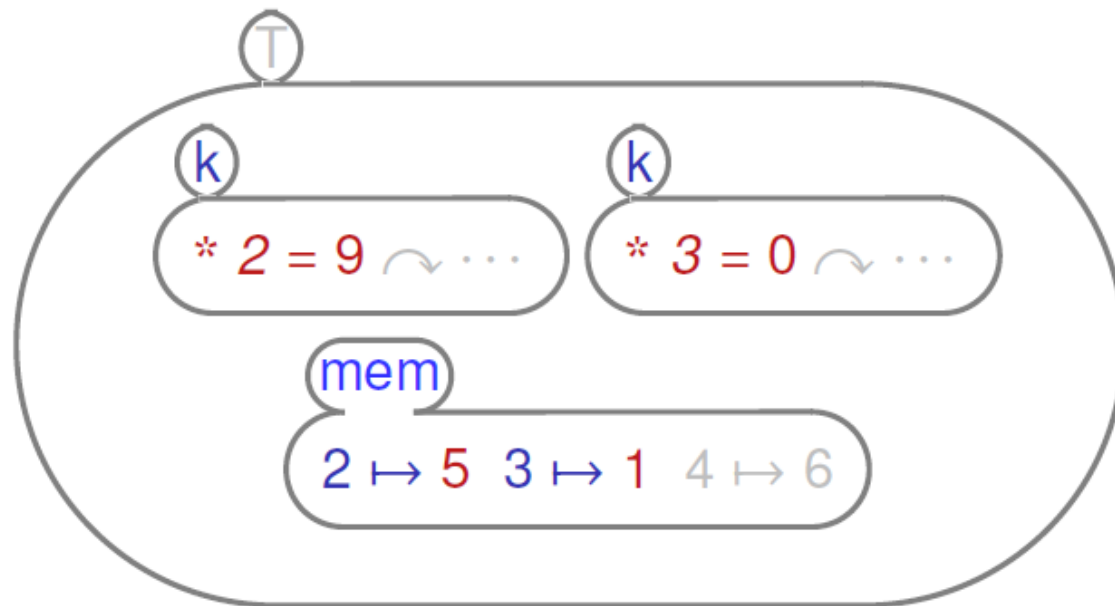
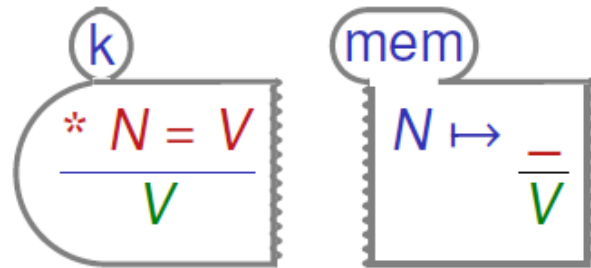
\mathbb{K} rules enhance concurrency

Concurrent updates (on distinct locations)



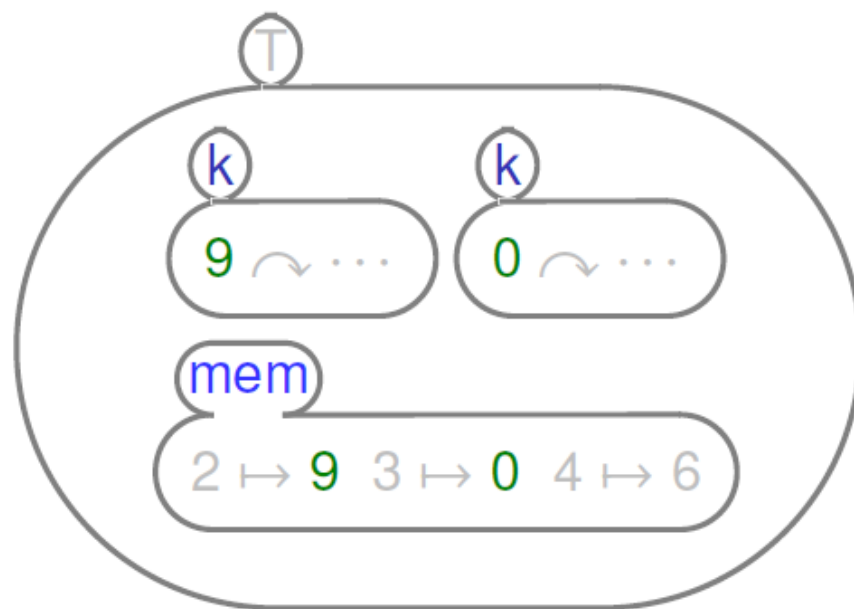
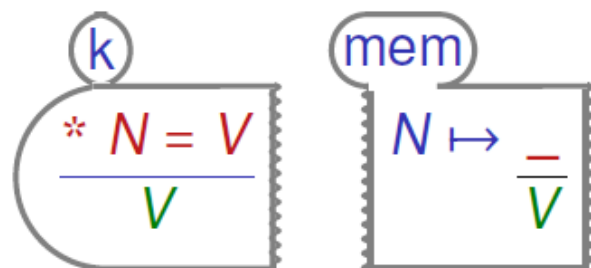
\mathbb{K} rules enhance concurrency

Concurrent updates (on distinct locations)



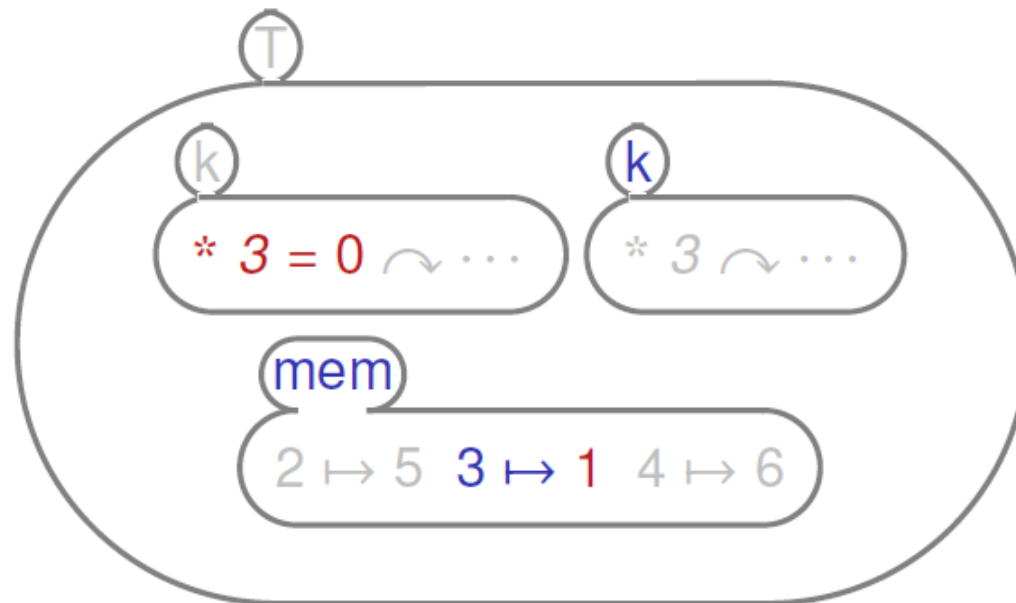
\mathbb{K} rules enhance concurrency

Concurrent updates (on distinct locations)



\mathbb{K} rules enhance concurrency

No dataraces: rule instances can overlap only on read-only part



Formally capturing the concurrency of \mathbb{K}

Idea: Give semantics to \mathbb{K} rewriting through graph rewriting

- \mathbb{K} rules resemble graph rewriting rules
- Graph rewriting captures concurrency with sharing of context

Results: A new formalism of term-graph rewriting

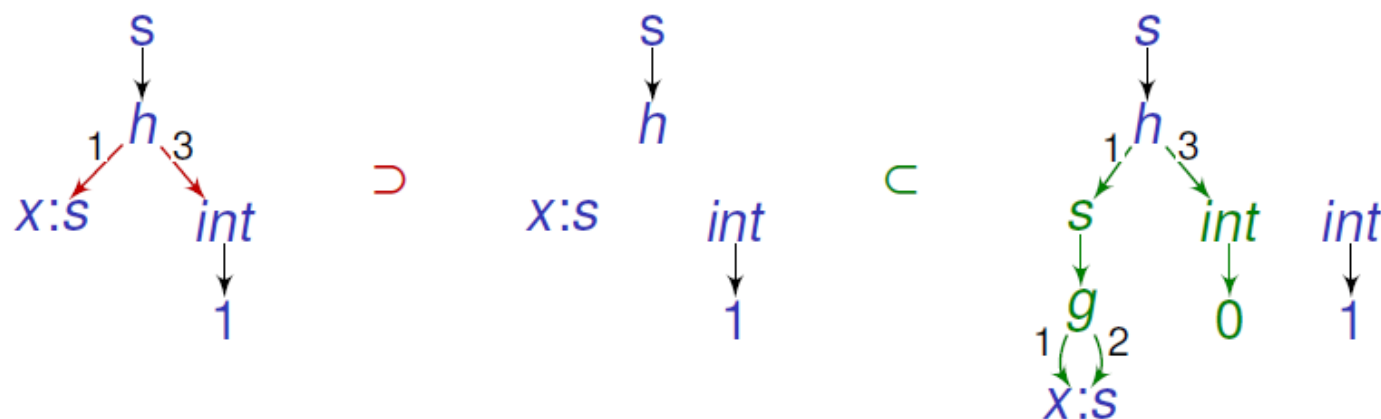
- Sound and complete w.r.t. term rewriting
- Capturing the intended concurrency of \mathbb{K} rewriting

\mathbb{K} graph rules: a new kind of term graph rewriting rules \mathbb{K} rule ρ

$$h\left(\frac{x}{g(x, x)}, -, 1\right)$$

Direct representation as a rewrite rule $K2R(\rho)$

$$h(x, y, 1) \rightarrow h(g(x, x), y, 0)$$

Corresponding graph rewrite rule $K2G(\rho)$ 

\mathbb{K} rewriting

Definition

Let \mathbb{S} be a \mathbb{K} rewrite system and t be a term. Then

$$t \xRightarrow[\mathbb{K}]{\mathbb{S}} t' \text{ iff } K2G(t) \xRightarrow[Graph]{K2G(\mathbb{S})} H \text{ such that } \text{term}(H) = t'$$

Theorem (Correctness w.r.t. rewriting)

Soundness: If $t \xRightarrow[\mathbb{K}]{\rho} t'$ then $t \xRightarrow[Rew]{K2R(\rho)} t'$.

Completeness: If $t \xRightarrow[Rew]{K2R(\rho)} t'$ then $t \xRightarrow[\mathbb{K}]{\rho} t'$.

Serializability: If $t \xRightarrow[\mathbb{K}]{\rho_1 + \dots + \rho_n} t'$ then $t \xRightarrow[\mathbb{K}]{\rho_1^*} \dots \xRightarrow[\mathbb{K}]{\rho_n^*} t'$ (and thus, $t \xRightarrow[Rew]{*} t'$).

Representing \mathbb{K} into RWL

Core of the K-Maude tool

Faithfully, in two steps

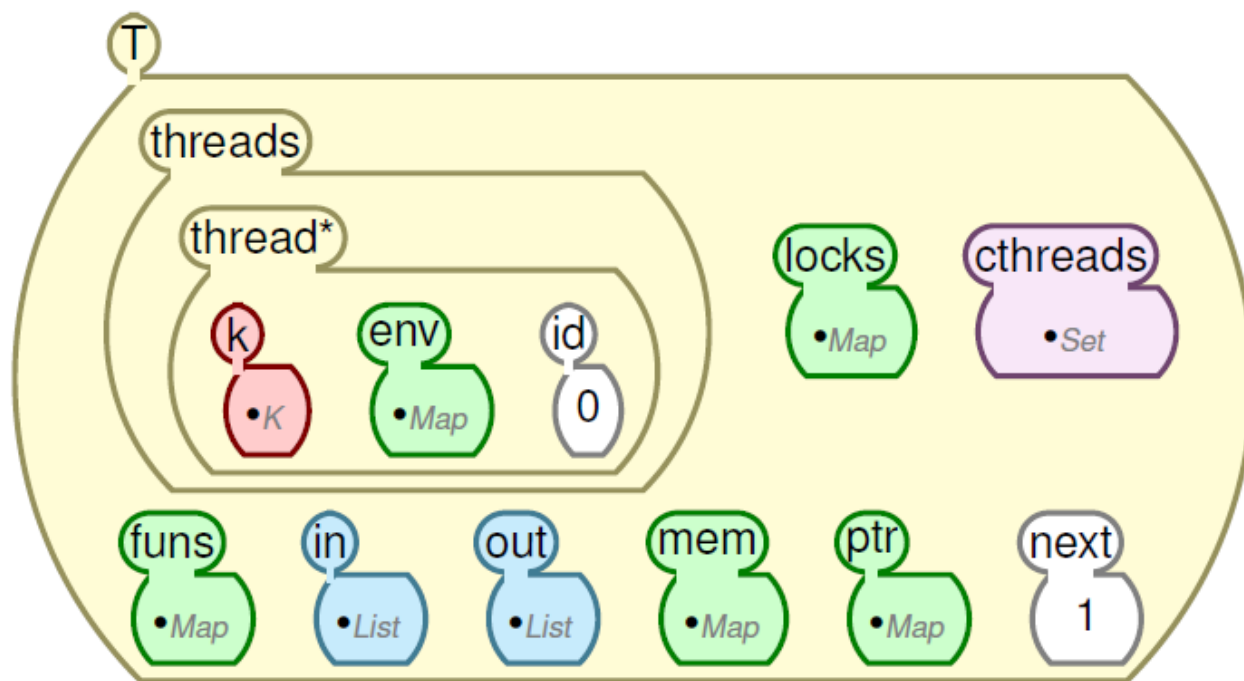
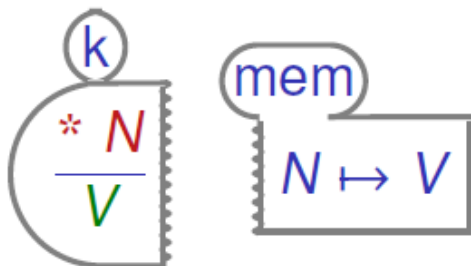
- Represent \mathbb{K} rewriting as \mathbb{K} graph rewriting
- RWL captures graph rewriting as theories over concurrent objects
 - Theoretical, non executable
- Useful to reason about the amount of concurrency in a \mathbb{K} definition

Directly—Implemented in the K-Maude tool

- Straight-forward, forgetting read-only information of \mathbb{K} rules
- Executable, with full access to rewriting logic's tools
- Loses the one-step concurrency of \mathbb{K}
 - Nevertheless, it is sound for all other analysis purposes

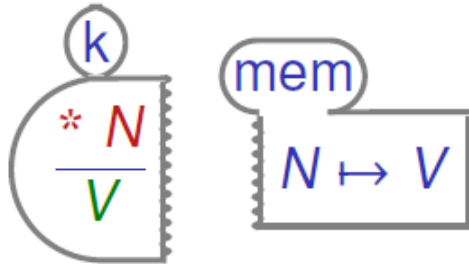
Direct embedding of \mathbb{K} into RWL (core of K-Maude)

Dereferencing rule

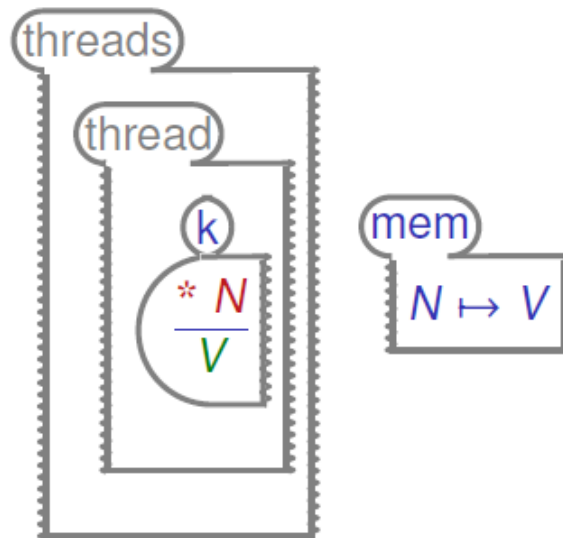


Direct embedding of \mathbb{K} into RWL (core of K-Maude)

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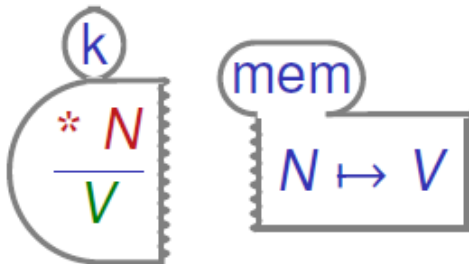


Configuration-concretized version of the rule



Direct embedding of \mathbb{K} into RWL (core of K-Maude)

Dereferencing rule



Flattening the \mathbb{K} rule to a rewrite rule

```

rl  <threads> <thread> <k> * N ↦ ?1:K </k> ?2:Bag </thread> ?3:Bag </threads>
    <mem> N ↦ V ?4:Map </mem>
=> <threads> <thread> <k> V ↦ ?1:K </k> ?2:Bag </thread> ?3:Bag </threads>
    <mem> N ↦ V ?4:Map </mem>
  
```

K-Maude overview *[Șerbănuță, Roșu, 2010]*

K-Maude compiler: 22 stages ~ 8k lines of Maude code

- Transforming \mathbb{K} rules in rewrite rules (6 stages)
- Strictness rules generation (3 stages)
- Flattening syntax to AST form (10 stages)
- Interface (3 stages)

K-L^AT_EX compiler—typesetting ASCII \mathbb{K}

- Graphical representation, for presentations
- Mathematical representation, for papers

K-Maude Demo?

From PL definitions to runtime analysis tools

K-Maude community

<http://k-framework.googlecode.com>

Current K-Maude projects

C Chucky Ellison

Haskell Michael Ilseman, David Lazar

Javascript Maurice Rabb

Scheme Patrick Meredith

X10 Milos Gligoric

Matching Logic Elena Naum, Andrei Ștefănescu

CEGAR Irina Asăvoae, Mihail Asăvoae

Teaching Dorel Lucanu, Grigore Roșu

Interface Andrei Arusoaie, Michael Ilseman

Summary of contributions

\mathbb{K} : a framework for defining real programming languages

- Expressive—at least as Reduction with evaluation contexts
- Modular—at least as Modular SOS
- Concurrent—more than CHAM
- Concise, intuitive

K-Maude: a tool for executing and analyzing \mathbb{K} definitions

- \mathbb{K} definitions become testing and analysis tools
- Strengthens the thesis that RWL is amenable for PL definitions

Related work using \mathbb{K}

Definitions of real languages “by the book”

- Java [*Farzan, Chen, Mesequer, Roşu, 2004*]
- Scheme [*Meredith, Hills, Roşu, 2007*]
- Verilog [*Meredith, Katelman, Mesequer, Roşu, 2010*]
- C [*Ellison, Roşu, 2011?*]

Analysis tools and techniques

- Static Policy Checker for C [*Hills, Chen, Roşu, 2008*]
- Memory Safety [*Roşu, Schulte, Şerbănuţă, 2009*]
- Type Soundness [*Ellison, Şerbănuţă, Roşu, 2008*]
- Matching Logic [*Roşu, Ellison, Schulte, 2010*]
- CEGAR with predicate abstraction [*Asăvoae, Asăvoae, 2010*]

Future Work

Rewriting & Programming languages

- Long list of feature requests for K-Maude
- Use of \mathbb{K} as a programming language
 - Compiling \mathbb{K} definitions for faster (and concurrent) execution
- Proving meta-properties about languages

Specifying and verifying concurrency

- Relaxed memory models

Foundations

- Explore non-serializable concurrency for rewriting
- Models for \mathbb{K} definitions